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FOR YOUR SYSTEM TO WORK PROPERLY, PLEASE DOWNLOAD APPROPRIATE

DRIVERS/IMAGES/USER'S MANUAL FROM THE LINKS BELOW:

- Manuals: <http://www.supermicro.com/support/manuals>
- Drivers & Utilities: <https://www.supermicro.com/wdl/driver/>
- Safety: http://www.supermicro.com/about/policies/safety_information.cfm

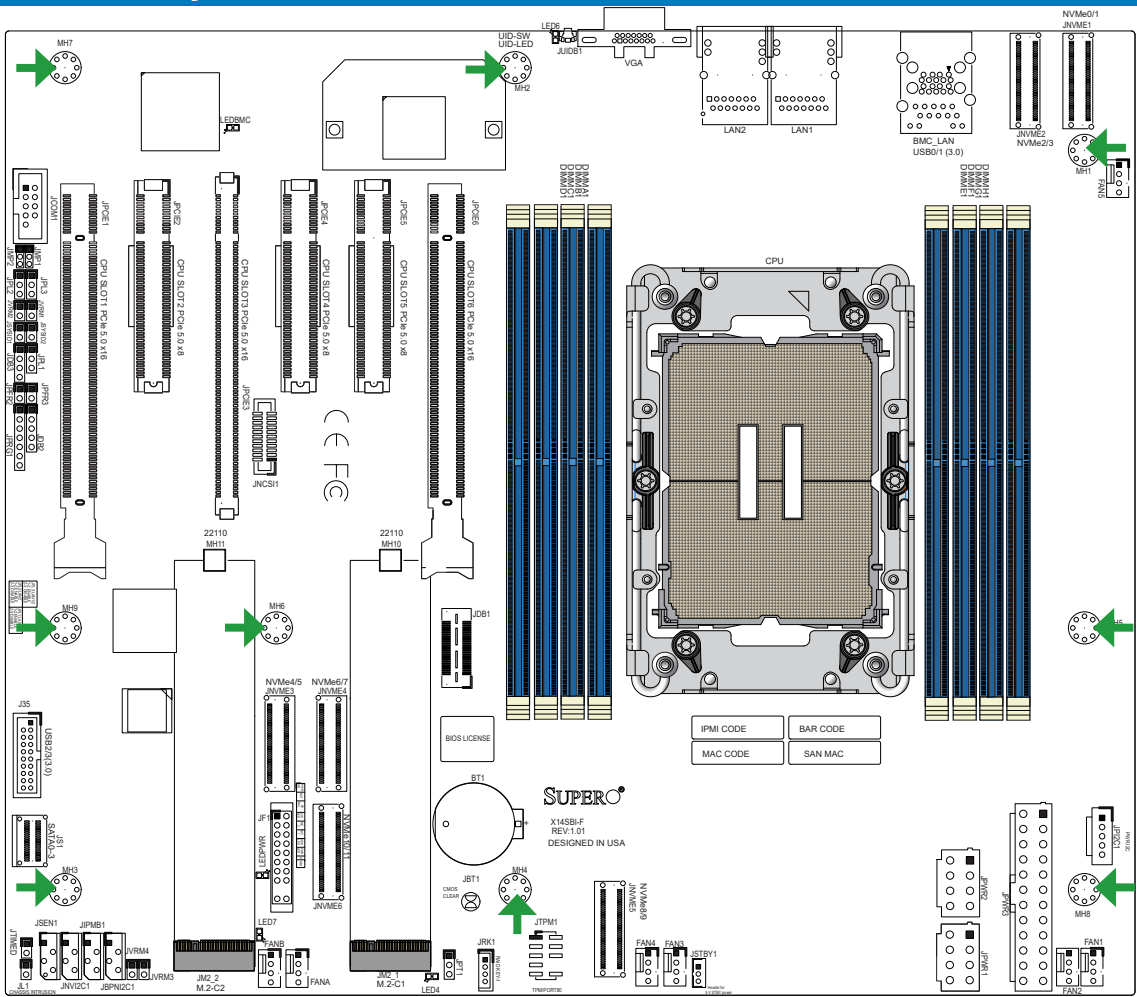
PACKAGE CONTENTS

- One Supermicro Motherboard
- One Quick Reference Guide
- One I/O Shield
- One SATA Cable
- Two CPU Carriers



WARNING: This product can expose you to chemicals including lead, known to the State of California to cause cancer and birth defects or other reproductive harm. For more information, go to www.P65Warnings.ca.gov.

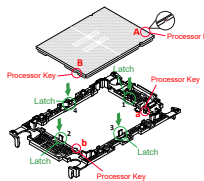
Motherboard Layout and Features



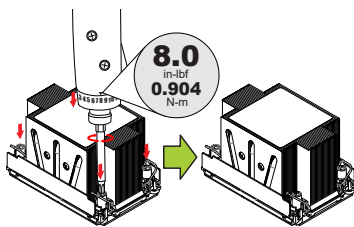
→ = mounting hole

Processor and PHM Installation

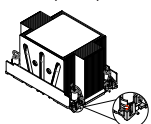
- 1 Assemble the processor carrier assembly by aligning and placing one end of the processor into the latch marked A, and place the other end of processor into the latch marked B.



- 2 After assembling the PHM, mount it onto the processor socket. With a T30 bit torque driver set to a force of 8.0 in-lbf (0.904 N-m), gradually tighten the four screws.



- 2 To form the processor heatsink module (PHM), mount the processor carrier assembly onto the heatsink and snap into place.



The processor carriers (SKT-1543H-0000-FXC and SKT-1544H-0000-FXC) are included in the shipping package.

Front Control Panel

| | 1 | 2 | |
|-------------|----|----|-------------------|
| PWR | ○ | ○ | GND |
| Reset | ○ | ○ | GND |
| | ○ | ○ | Power Fail LED |
| +3.3 V | ○ | ○ | OH/Fan Fail LED |
| UID LED | ○ | ○ | NIC2 Activity LED |
| +3.3 V Stby | ○ | ○ | NIC1 Activity LED |
| +3.3 V Stby | ○ | ○ | HDD LED |
| +3.3 V Stby | ○ | ○ | PWR LED |
| 3.3 V Stby | ○ | ○ | X |
| X | ○ | ○ | X |
| NMI | ○ | ○ | Ground |
| | 19 | 20 | |

Jumpers and Connectors

| Jumpers | | |
|---------|---|---|
| Jumper | Description | Default Setting |
| JBT1 | CMOS Clear | Open (Normal) |
| JPL1 | LAN1 Enable/Disable (I210, X14SBI-F only) | Pins 1-2 (Enabled) Pins 2-3 (Disabled) |
| JPL2 | LAN2 Enable/Disable (I210, X14SBI-F only) | Pins 1-2 (Enabled) Pins 2-3 (Disabled) |
| JPL3 | LAN 1/2 Enable/Disable (X550, X14SBI-TF only) | Pins 1-2 (Enabled) Pins 2-3 (Disabled) |
| JPT1 | TPM 2.0 Enable/Disable | Pins 1-2 (Enabled) Pins 2-3 (Disabled) |
| JVRM1 | VRM SMB Clock (to BMC) | Pins 1-2 (Normal) |
| JVRM2 | VRM SMB Data (to BMC) | Pins 1-2 (Normal) |

| Connectors | |
|-----------------------|--|
| Connector | Description |
| BMC_LAN | Dedicated BMC LAN Port |
| BT1 | Onboard Battery |
| FAN1-FAN5, FANA, FANB | Processor/System Fan Headers (FAN1: CPU Fan) |
| J35 | USB 3.2 Gen 1 Header (USB2/3) |
| JBPNI ² C1 | 4-pin BMC External I ² C Header |
| JCOM1 | COM Header |
| JF1 | Front Control Panel Header |
| JIPMB1 | System Management Bus Header (for IPMI only) |
| JL1 | Chassis Intrusion Header |
| JM2_1, JM2_2 | M.2 M-Key PCIe 5.0 x2 Slots (supports 22110 form factor) |
| JNCSI1 | NC-SI Port Selection |
| JNVI ² C1 | Non-volatile Memory (NVMe) I ² C Header |
| JNVME1-JNVME6 | MCIO PCIe 5.0 x8 Connectors (JNVME3-JNVME6 are enabled with the following processors installed: Intel® Xeon® 6781P, 6761P, 6741P, 6731P, 6521P, 6511P) |
| JPI ² C1 | Power I ² C System Management Bus (Power SMB) Header |
| JPWR1, JPWR2 | 8-pin 12 V CPU Power Connector |
| JPWR3 | 24-pin ATX Power Connector (Required) |
| JRK1 | Intel® RAID Key Header |
| JS1 | Slimline SAS SATA 3.0 Ports |
| JSEN1 | Inlet Sensor Header |
| JSTBY1 | Standby Power Header |
| JTPM1 | Trusted Platform Module/Port 80 Header |
| JUIDB1 | Unit Identifier Button |
| LAN1, LAN2 | LAN (RJ45) Ports |
| MH1-MH9 | Mounting Holes |
| MH10, MH11 | M.2 Mounting Holes |

Note: Refer to Chapter 1 of the User Manual for detailed information on jumpers, connectors, and LED indicators.

Connectors and LED Indicators

| Connectors | |
|------------|--|
| Connector | Description |
| SLOT1 | PCIe 5.0 x16 Slot with Latch |
| SLOT2 | PCIe 5.0 x8 Slot |
| SLOT3 | PCIe 5.0 x16 Slot |
| SLOT4 | PCIe 5.0 x8 Slot |
| SLOT5 | PCIe 5.0 x8 Slot |
| SLOT6 | PCIe 5.0 x16 Slot with Latch (SLOT6 is enabled with the following processors installed: Intel® Xeon® 6781P, 6761P, 6741P, 6731P, 6521P, 6511P) |
| USB0/1 | I/O Panel USB 3.2 Gen 1 Ports |
| VGA | VGA Port |

| LED Indicators | | |
|----------------|---------------------|--------------------------------|
| LED | Description | Status |
| LED4 | M.2 LED | Blinking: Device Working |
| LED6 | Unit Identifier LED | Solid Blue: Unit Identified |
| LED7 | M.2 LED | Blinking: Device Working |
| LEDBMC | BMC Heartbeat | Blinking Green: Device Working |
| LEDPWR | Power LED | Solid Green: Power On Blinking |

Processor and Memory Support

The X14SBI-F-TF motherboard supports the Intel® Xeon® 6700/6500-series processors with P-cores or Intel® Xeon® 6700-series processor with E-cores, and a thermal design power (TDP) of up to 350 W.

The motherboard supports up to 2 TB of 3DS RDIMM/MRDIMM DDR5 ECC memory with speeds of up to 8000 MT/s in eight DIMM slots.

| Intel Xeon 6700/6500 Series Processors with P-Cores DDR5 Memory Population Table (1 Processor and 8 DIMMs Installed, 1DPC) | |
|---|--|
| DIMM Counts | Memory Population Sequence (1DPC) |
| 1 | DIMMA1 |
| 4 | DIMMA1/DIMMC1/DIMME1/DIMMG1 DIMMB1/DIMMD1/DIMMF1/DIMMH1 |
| 8 | DIMMA1/DIMMB1/DIMMC1/DIMMD1/DIMME1/DIMMF1/DIMMG1/DIMMH1 |

| Intel Xeon 6700 Series Processors with E-Cores DDR5 Memory Population Table (1 Processor and 8 DIMMs Installed, 1DPC) | |
|--|---|
| DIMM Counts | Memory Population Sequence (1DPC) |
| 1 | DIMMA1 |
| 8 | DIMMA1/DIMMB1/DIMMC1/DIMMD1/DIMME1/DIMMF1/DIMMG1/DIMMH1 |

I/O Ports

| # | Description | # | Description |
|----|------------------|----|-------------|
| 1. | BMC_LAN | 5. | LAN2 |
| 2. | USB0 (3.2 Gen 1) | 6. | VGA |
| 3. | USB1 (3.2 Gen 1) | 7. | UID Button |
| 4. | LAN1 | | |

Note: Refer to Chapter 2 of the User Manual for detailed information on memory support and processor/motherboard installation instructions.

Note: Graphics shown in this quick reference guide are for illustrative purposes only. Your components may or may not look exactly the same as drawings shown in this guide.