

# Broadcom® 95xx PCle 4.0 MegaRAID™ and HBA Tri-Mode **Storage Adapters**

**User Guide** Version 2.10

95xx-MR-HBA-Tri-Mode-UG111 Broadcom

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# Broadcom PCIe 4.0 MegaRAID and HBA Tri-Mode Storage Adapters

This document is the primary reference and user guide for the Broadcom<sup>®</sup> PCIe 4.0 MegaRAID<sup>™</sup> tri-mode storage adapters and Broadcom PCIe 4.0 HBA tri-mode storage adapters, based on the Broadcom PCIe 4.0 tri-mode controller devices. This document contains the complete installation instructions and specifications for the following PCIe 4.0 tri-mode storage adapters, referred to as adapters.

- MegaRAID 9560-16i
- MegaRAID 9560-8i
- MegaRAID 9580-8i8e
- MegaRAID 9540-8i
- MegaRAID 9562-16i
- HBA 9500-16i
- HBA 9500-8i
- HBA 9500-16e
- HBA 9500-8e
- HBA 9502-16i

#### **Overview**

The adapters, based on the SAS3916, SAS3908, SAS3816, or SAS3808 tri-mode controller, are high-performance PCIe-to-SATA/SAS/PCIe (tri-mode) storage adapters. Broadcom tri-mode SerDes technology enables operation of SAS, SATA, or PCIe (NVMe) storage devices in a single drive bay. A single controller can operate in all three modes concurrently: SAS, SATA, and PCIe/NVMe. The adapters negotiate between the speeds and the protocols to recognize and concurrently interface with these three storage devices types.

The adapters provide the following storage interface data transfer rates:

- SAS data transfer rates of 12Gb/s, 6Gb/s, and 3Gb/s per phy
- SATA transfer rates at 6Gb/s and 3Gb/s per phy
- PCIe (NVMe) data transfer rates of 16 GT/s, 8 GT/s, 5 GT/s, and 2.5 GT/s per lane

The following tables summarize key adapter features.

**Table 1: MegaRAID Tri-Mode Storage Adapter Features** 

Adapter	9560-16i	9560-8i	9580-8i8e	9540-8i	9562-16i
Ports	16 internal	8 internal	8 internal, 8 external	8 internal	16 internal
I/O Processor	SAS3916	SAS3908	SAS3916	SAS3808	SAS3916
Form Factor	LP-MD2	LP-MD2	LP-MD2	LP-MD2	OCP NIC 3.0 SFF
Storage Interface Connectors	Two SFF-8654 x8	One SFF-8654 x8	One SFF-8654 x8, Two SFF-8644 x4	One SFF-8654 x8	Two SFF-8654 x8 (vertical)
Host Interface	x8 PCIe 4.0	x8 PCle 4.0	x8 PCIe 4.0	x8 PCle 4.0	x8 PCIe 4.0
Storage Interface	SAS, SATA, and PCle (NVMe)	SAS, SATA, and PCle (NVMe)	SAS, SATA, and PCle (NVMe)	SAS, SATA, and PCle (NVMe)	SAS, SATA, and PCle (NVMe)

Adapter	9560-16i	9560-8i	9580-8i8e	9540-8i	9562-16i
Cache Memory	8 GB, 2666 MT/ s, DDR4 SDRAM	4 GB, 2666 MT/ s, DDR4 SDRAM	8 GB, 2666 MT/ s, DDR4 SDRAM	_	8 GB, 2666 MT/ s, DDR4 SDRAM
Cache Protection	Yes	Yes	Yes		Yes
Super Capacitor	CVPM05 module	CVPM05 module	CVPM05 module	<u> </u>	CVPM05 module

**Table 2: HBA Tri-Mode Storage Adapter Features** 

Adapter	9500-16i	9500-8i	9500-16e	9500-8e	9502-16i
Ports	16 internal	8 internal	16 external	8 external	16 internal
I/O Processor	SAS3816	SAS3808	SAS3816	SAS3808	SAS3816
Form Factor	LP-MD2	LP-MD2	LP-MD2	LP-MD2	OCP NIC 3.0 SFF
Storage Interface Connectors	Two SFF-8654 x8	One SFF-8654 x8	Four SFF-8644 x4	Two SFF-8644 x4	Two SFF-8654 x8 (vertical)
Host Interface	x8 PCIe 4.0	x8 PCle 4.0	x8 PCle 4.0	x8 PCIe 4.0	x8 PCIe 4.0
Storage Interface	SAS, SATA, and PCle (NVMe)	SAS, SATA, and PCle (NVMe)	SAS, SATA, and PCle	SAS, SATA, and PCIe	SAS, SATA, and PCle (NVMe)

### **Features**

#### **RAID Features**

The following list includes primary RAID features that the MegaRAID adapters support. For a full description of the RAID features, refer to the 12Gb/s MegaRAID Tri-Mode Software User Guide, located at http://www.broadcom.com/support/download-search.

- RAID levels 00, 0, 1, 5, and 6
- RAID spans 10, 50, and 60
- JBOD mode with RAID0, RAID1, and RAID10
- · JBOD for SDS environments
- Online Capacity Expansion (OCE)
- Auto resume after loss of system power during array rebuild or OCE
- Single controller multipathing
- Load balancing
- Configurable stripe size up to 1 MB
- · Fast initialization for quick array setup
- · Check Consistency for background data integrity
- SSD support with SSD Guard<sup>™</sup> technology
- Patrol read for media scanning and repairing
- Disk data format (DDF)-compliant Configuration on Disk (COD)
- Self-Monitoring, Analysis, and Reporting Technology (S.M.A.R.T) support
- · Global and dedicated hot spare with revertible hot spare support
- Automatic rebuild
- Enclosure affinity
- · Emergency SATA hot spare for SAS arrays
- · Enclosure management
- SCSI Enclosure Services (SES) (inband)
- SGPIO (sideband)
- DataBolt bandwidth optimizer technology support for compatible expander-based enclosures
- · Shield state drive diagnostic technology

## **Operating System Support**

The tri-mode storage adapters support the operating systems in the following list. For specific version information, refer to the *MegaRAID Tri-Mode Device Driver Installation User Guide*, located at http://www.broadcom.com/support/downloadsearch.

- Microsoft Windows
- VMware vSphere/ESXi
- · Red Hat Enterprise Linux
- SuSE Linux
- Ubuntu Linux
- Citrix XenServer
- CentOS Linux
- Debian Linux
- Oracle Enterprise Linux
- Fedora
- FreeBSD

The firmware and drivers are routinely updated and made available on the Broadcom Support and Download center. Visit <a href="http://www.broadcom.com/support/download-search">http://www.broadcom.com/support/download-search</a> and download the latest firmware and driver for the adapter.

### **PCIe Host Interface**

The adapter's PCIe 4.0 host interface provides maximum transmission and reception rates of up to 128 GT/s (16GB/s per lane). The tri-mode controller uses a packet-based communication protocol to communicate over the serial interconnect. Other PCIe host interface features include the following:

- · Eight-lane PCIe host interface
- PCIe Hot-Plug
- Power management:
  - Supports the PCI Bus Power Management Interface Specification Revision 1.2
  - Supports Active State Power Management, including the L0 states, by placing links in a power-saving mode during times of no link activity
- Error handling
- High bandwidth per pin with low overhead and low latency
- · Lane reversal and polarity inversion
- Single-phy (one-lane) link transfer rate of 16 GT/s, 8 GT/s, 5 GT/s, and 2.5 GT/s in each direction
- Eight-lane aggregate bandwidth of up to 16GB/s (16,000 MB/s)
- Support of x8, x4, x2, and x1 link widths

### **LED Management**

The internal adapters offer LED management support for SAS/SATA backplanes and (PCIe) NVMe backplanes. External connect adapters offer enclosure LED management support for enclosure implementations through SES. See Backplane Management for more information.

## Tri-Mode Storage Interface Features

The adapter's storage interface supports concurrent operation with SAS, SATA, and PCIe (NVMe) devices to provide a fully functional solution for any storage environment.

- PCIe (NVMe) interface features:
  - Up to sixteen x1, eight x2, or four x4 NVMe direct-attach drive support
  - Data transfer at 16 GT/s, 8 GT/s, 5 GT/s, and 2.5 GT/s
  - Independent resets and configuration
  - Common reference clock and separate reference clock independent SSC (SRIS) support
- SAS features:

- SAS data transfers at 12Gb/s, 6Gb/s, and 3Gb/s
- DataBolt technology on all SAS phys to improve performance
- Serial, point-to-point, enterprise-level storage interface
- Wide ports that contain multiple phys
- Narrow ports that contain a single phy
- SAS phy power management
- Data transfer by using SCSI information units
- T10 data protection management
- Support for persistent connection capability
- Support for SPL-3 initiate close capability
- Configurable Rx and Tx polarity inversion
- Configurable phy-to-disk mapping
- Configurable SSC
- SATA interface features:
  - SATA and STP data transfers at 6Gb/s and 3Gb/s
  - Addressing of multiple SATA targets through an expander

# **Tri-Mode Storage Interface**

The internal adapters can direct attach to SAS, SATA, or NVMe drives. The internal and external adapters support drive attach through PCIe switches or expanders.

#### NOTE

Carefully assess any decision to mix SAS and SATA drives within the same virtual drive (VD). Although you can mix drives, the practice is discouraged.

MegaRAID does not permit mixing SAS and NVMe drives or SATA and NVMe drives within the same VD. To mix NVMe and SAS/SATA drives on a MegaRAID adapter, you must configure the drives in separate VDs.

## **SAS/SATA Support**

The adapters support internal and external storage devices, which allow you to use a system that supports enterprise-class SAS drives and desktop-class SATA III drives.

The storage interface is comprised of either 16 phys or 8 phys, depending on the controller. Dedicated hardware manages the phys in groups of eight, in ascending phy order. One dedicated instance of the SAS phy management hardware manages PHY 0 to PHY 7, and a separate instance of the SAS phy management hardware manages PHY 8 to PHY 15. These SAS phy management hardware instances, or SAS cores, cannot communicate with each other.

When you configure a wide port, the connections must attach exclusively to phys all managed by the same SAS core. If the ports are not managed by the same SAS core, unexpected controller and host behavior occurs. Port 0 to port 7 can be configured as eight separate ports or combined into one or more groups called wide ports (one x4, two x4s, one x8, and so on). Similarly, port 8 to port 15 can be configured as eight separate ports or combined into one or more wide ports. A single wide port cannot combine individual ports or phys sourced by different SAS cores.

The following table indicates the connector-to-SAS core mapping for each adapter. The card layout figures in Broadcom MegaRAID and HBA Tri-Mode Storage Adapter Characteristics show the connector designations for each adapter.

Table 2.	A dantar	Dout to	CAC Do	t Associations
Table 3:	Adabter	Port-to-	-SAS POI	t Associations

Adapter	Connector 0	Connector 1	Connector 2	Connector 3
9560-16i	SAS Core 0	SAS Core 1	N/A	N/A
9560-8i	SAS Core 0	N/A	N/A	N/A
9580-8i8e	SAS Core 1	SAS Core 0	SAS Core 0	N/A
9540-8i	SAS Core 0	N/A	N/A	N/A
9562-16i	SAS Core 0	SAS Core 1	N/A	N/A
9500-16i	SAS Core 0	SAS Core 1	N/A	N/A
9500-8i	SAS Core 0	N/A	N/A	N/A
9500-16e	SAS Core 0	SAS Core 0	SAS Core 1	SAS Core 1
9500-8e	SAS Core 0	SAS Core 0	N/A	N/A
9502-16i	SAS Core 0	SAS Core 1	N/A	N/A

When you configure a boot device in a multipath environment, the target must connect to one or more ports on the same SAS core with AutoPortConfig enabled. The boot device appears to the host system as a single device on the active path. The multipath environment manages a different controller as the passive path.

When you configure data-storage devices in a multipath environment, the rule for creating wide ports applies, but multiple ports from different SAS cores can connect to the data-storage devices. The multipath environment manages data-storage devices that the controller presents more than once.

### PCIe (NVMe) Support

The following table shows how many NVMe drives or Broadcom PEX88000-series switches can directly attach to each adapter. The HBA 9500-16e and HBA 9500-8e adapters do not support direct attach to NVMe drives. The expected topology for the HBA 9500-16e and HBA 9500-8e adapters is a typical JBOF scenario that uses a switch to connect the NVMe drives.

The adapters do not support switch connections wider than x4 and one level deep.

Table 4: NVMe Devices or PCIe Switch Direct-Attach Options Supported for Each Adapter

Adapter	x4 NVMe Drives	x2 NVMe Drives	x1 NVMe Drives	x4 Switch
9560-16i	4	8	16	4
9560-8i	2	4	8	2
9580-8i8e	2	4	8	4
9540-8i	2	4	4	0
9562-16i	4	8	16	4
9500-16i	4	8	16	4
9500-8i	2	4	8	2
9500-16e	0	0	0	4
9500-8e	0	0	0	2
9502-16i	4	8	16	4

The tri-mode device interface contains a SAS core and a PCle device bridge (PDB). The PDB enables the PCle (NVMe) storage interface connections, and each PDB can support direct connect to NVMe devices or to x4 PCle switches. The storage interface is comprised of 16 phys or 8 phys depending on the controller. One PDB manages PHY 0 to PHY 7, and a second PDB manages PHY 8 to PHY 15. The PDBs cannot communicate with each other. This means that a PCle port of greater than one lane must attach exclusively to phys all managed by the same PDB and must be comprised of adjacent lanes.

The following table indicates how the connectors map to the PDB for each adapter. The card layout figures in Broadcom MegaRAID and HBA Tri-Mode Storage Adapter Characteristics show the connector designations for each adapter.

**Table 5: Adapter Lanes-to-PDB Core Associations** 

Adapter	Connector 0	Connector 1	Connector 2	Connector 3
9560-16i	PDB 0	PDB 1	N/A	N/A
9560-8i	PDB 0	N/A	N/A	N/A
9580-8i8e	PDB 1	PDB 0	PDB 0	N/A
9540-8i	PDB 0	N/A	N/A	N/A
9562-16i	PDB 0	PDB 1	N/A	N/A
9500-16i	PDB 0	PDB 1	N/A	N/A

Adapter	Connector 0	Connector 1	Connector 2	Connector 3
9500-8i	PDB 0	N/A	N/A	N/A
9500-16e	PDB 0	PDB 0	PDB 1	PDB 1
9500-8e	PDB 0	PDB 0	N/A	N/A
9502-16i	PDB 0	PDB 1	N/A	N/A

# **Common REFCLK Support**

The adapter uses x8 SFF-8654 (SlimSAS) connectors. Each connector is divided into two quadrants. For connections that require a common REFCLK, one REFCLK is supplied for each quadrant. For x4 NVMe or PCle switch connections, the REFCLK sourced by each quadrant directly clocks each attached x4 PCle connection.

To directly attach x2 or x1 NVMe drives that require a common REFCLK, where more than one drive is sourced from a single quadrant, you must properly fan out the shared REFCLK on the backplane. For x2 and x1 NVMe connections, use SRIS-enabled drives to avoid fanning the clock out on the backplane.

# **Backplane Management**

The SFF-8448 standard defines how to detect whether the backplane supports a SGPIO or two-wire interface (I<sup>2</sup>C) for SAS/SATA usage. SFF-9402 is a superset of SFF-8448, adding the PCIe-defined sideband signal, which means that SAS/SATA users see no change in backplane management detection when using the adapters.

### **Universal Backplane Management**

The adapters provide LED operation and other backplane management of NVMe only, SAS/SATA only, or mixed-protocol backplanes based on the SFF-TA-1005 specification. SFF-TA-1005 is an industry-standard backplane management specification commonly known as Universal Backplane Management (UBM). As long as the backplane management controller is designed in accordance with the UBM specification, the adapter automatically detects the backplane type and functions appropriately.

The adapter supports the industry-standard *SFF-TA-1005 Specification for Universal Backplane Management (UBM)*. UBM provides the following key features:

- Reports the backplane capabilities, including the following:
  - NVMe drive widths
  - Common REFCLK or separate REFCLK support
  - Maximum speeds
  - Designed slot power
- · Supports cable order independence, that is, the drive LED control and slot ID are not dependent on cable order.
- · Enables drive hot plug insertion through control of PERST# timing.

For existing SAS/SATA backplanes, if BP\_TYPE = 0, the adapter uses SGPIO for legacy backplane management. Refer to the SFF-8485 specification for functionality details. Design new backplanes with the industry-standard SFF-TA-1005 (UBM) specification for backplane management.

### **Virtual Pin Port Management**

Broadcom requires new designs to enable UBM for backplane management. The adapter maintains support for Virtual Pin Port (VPP) backplane management for legacy implementations. The adapters provide LED operation for NVMe devices based on the VPP over I<sup>2</sup>C definition. Standard VPP implementation calls for one PCA9555 target per two devices. For each drive pair, the adapter expects to see one PCA9555 target responding to address 0x40 on each pair of NVMe drives.

### **Sideband Signals**

The internal adapters have one or two x8 SFF-8654 connectors. Each x8 connector provides two sets of sidebands. This section describes the sideband signal usage. The following table defines the sideband signal's pins on the SFF-8654 connector. The last column in the table indicates the strength of the pull-up resistor or pull-down resistor values on the adapter. See Table 7, Sideband Management Pin Settings, for the signal descriptions, and see Table 8, Internal x8 SFF-8654 Connector Pinout, for a complete connector pinout.

**Table 6: Sideband Signal Pinout** 

Connector A Side	Connector B Side	Sideband or Vendor Specific Pin Number	UBM Assignments	Direction	Resistor Value
A8	A26	7	BP_TYPE	Input	100 kΩ pull-down
A9	A27	4	2W_RESET#	Output	2.0 kΩ pull-up

Connector A Side	Connector B Side	Sideband or Vendor Specific Pin Number	UBM Assignments	Direction	Resistor Value
A10	A28	3	GND	_	_
A11	A29	+	REFCLK+	Output	_
A12	A30	-	REFCLK-	Output	_
B8	B26	0	2W_CLK	Input/Output	2.0 kΩ pull-up
B9	B27	1	2W_DATA	Input/Output	2.0 kΩ pull-up
B10	B28	2	GND	_	_
B11	B29	5	PERST#	Input/Output	2.0 kΩ pull-up
B12	B30	6	C_TYPE, D_INPL#, CHANGE_DET#	Input/Output	10 kΩ pull-up

The following table describes the sideband signal pin settings.

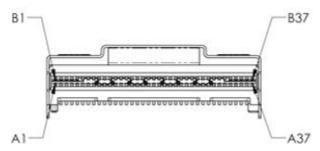
**Table 7: Sideband Management Pin Settings** 

Pin Name	Settings	Description	
BP_TYPE	0: SGPIO     1: Two-wire interface	Indicates if the backplane uses SGPIO or two-wire interface for management.  To maintain backwards compatibility with SPGIO-based backplanes, the adapter has a weak pull-down to default to SGPIO if the backplane does not explicitly drive the signal.	
2W_RESET#	0: Reset is asserted     1: Reset is not asserted	Optional reset driven by the host if the UBM target reports that the target can be reset.	
REFCLK+/-	_	PCIe REFCLK HCSL 100-MHz clock driven by the device side ports to PCIe devices that require REFCLK.  If D_INPL# is 0 and BP_TYPE is 1, the adapter enables the REFCLK outputs for that quad of high-speed lanes.  When BP_TYPE is 0 or the UBM Clock Routing bit on the backplane is 0, this output is turned off.	
2W_CLK	_	The two-wire interface clock signal.	
2W_DATA	_	The two-wire interface data signal.	
PERST#	0: Reset is asserted     1: Reset is not asserted	The adapter drives the PCIe RESET# signal.  This signal uses a clamp to ground so that the signal on the adapter powers up LOW until backplane detection warrants the release of this signal for open-drain use. This method ensures that PERST# does not deassert until the directly connected NVMe drive is successfully detected.	
C_TYPE, D_INPL#, CHANGE_DET#	_	<ul> <li>Open collector/drain input or output signal.</li> <li>C_TYPE. If BP_TYPE is 0, the adapter drives this signal LOW. If BP_TYPE is 1, this signal adheres to the SFF-8448 requirement to drive this signal to 1 in response to floating the signal. Because this signal is an open drain signal, driving to 1 is when a pull-up resistor pulls this signal HIGH.</li> <li>D_INPL#. When C_TYPE is HIGH, the backplane pulls this signal to ground to indicate an NVMe device is connected and a two-wire interface backplane management target might be on the sideband's two-wire interface.</li> <li>CHANGE_DET#. If D_INPL# is 0 and a UBM FRU device is discovered on the two-wire interface, the UBM FRU data can inform the adapter that the device is CHANGE_DET# feature capable. The adapter can rely on this signal as the CHANGE_DET# signal as described in the UBM specification. In this mode, the UBM controller drives this signal LOW to assert CHANGE_DET#.</li> </ul>	

# **Cables and Cabling Configurations**

The internal adapter follows the SFF-9402 standard for connector signal assignments. Each x8 connector includes eight PCIe transmit and receive lanes and two sets of sidebands designated as A and B, in accordance with the SFF-9402 specification. The following figure shows the x8 SFF-8654 pin designations.

Figure 1: x8 SFF-8654 Pin Designations



The following table defines the adapter's internal x8 SFF-8654 connector pinouts.

Table 8: Internal x8 SFF-8654 Connector Pinout

Pin	Name	Pin	Name
A1	GND	B1	GND
A2	PERp0, RX0+	B2	PETp0, TX0+
A3	PERn0, RX0-	В3	PETn0, TX0-
A4	GND	B4	GND
A5	PERp1, RX1+	B5	PETp1, TX1+
A6	PERn1, RX1-	B6	PETn1, TX1-
A7	GND	B7	GND
A8	BP_TYPEA	B8	2W-CLKA, SClockA
A9	2W_RESETA,SDataOutA	В9	2W-DATAA, SloadA
A10	GND	B10	GND
A11	REFCLKA+	B11	PERSTA#, SDatainA
A12	REFCLKA-	B12	CPRSNTA#, CNTRLR_TYPEA
A13	GND	B13	GND
A14	PERp2, RX2+	B14	PETp2, TX2+
A15	PERn2, RX2-	B15	PETn2, TX2-
A16	GND	B16	GND
A17	PERp3, RX3+	B17	PETp3, TX3+
A18	PERn3, RX3-	B18	PETn3, TX3-
A19	GND	B19	GND
A20	PERp0, RX4+	B20	PETp0, TX4+

Pin	Name	Pin	Name
A21	PERn0, RX4-	B21	PETn0, TX4-
A22	GND	B22	GND
A23	PERp1, RX5+	B23	PETp1, TX5+
A24	PERn1, RX5-	B24	PETn1, TX5-
A25	GND	B25	GND
A26	BP_TYPEB	B26	2W-CLKB, SClockB
A27	2W_RESETB,SDataOutB	B27	2W-DATAB, SLoadB
A28	GND	B28	GND
A29	REFCLKB+	B29	PERSTB#, SDataInB
A30	REFCLKB-	B30	CPRSNTB#, CNTRLR_TYPEB
A31	GND	B31	GND
A32	PERp2, RX6+	B32	PETp2, TX6+
A33	PERn2, RX6-	B33	PETn2, TX6-
A34	GND	B34	GND
A35	PERp3, RX7+	B35	PETp3, TX7+
A36	PERn3, RX7-	B36	PETn3, TX7-
A37	GND	B37	GND

### **Storage Interface Cabling**

Choose the proper cable for the given backplane type and connectors. The correct choice is especially important for backplanes that use SFF-8643 for the NVMe connectors. Many of these backplanes use an older legacy-recommended pinout for the NVMe connector instead of a connector pinout based on the SFF-9402 specification. Most backplanes that use either SFF-8612 or SFF-8654 connectors follow the SFF-9402 specification. The pinout recommended in the *PCI Express OCuLink Specification* is equivalent to that recommended for SFF-9402. Verify the connector pinout for the intended backplane to make sure the proper cable is used when connecting to NVMe drives.

Broadcom provides the following cables to use for the adapter. Use the MPN listed to order a cable from Broadcom. If you source your own cables, use the Broadcom part number from the following table, and the drawings and pinouts in Cable Drawings and Pinouts.

**Table 9: Internal Adapter Cables** 

MPN	Broadcom Cable Part No.	Cable Description	Backplane Connector
05-60001-00	5067-6865	x8 8654 to 2x4 8612, AltWiring 1M	Two x4 SFF-8612 (OCuLink)
05-60002-00	5067-6862	x8 8654 to 2x4 8643 (W), SMC 1M	Two x4 SFF-8643 (mini-SAS HD)
05-60003-00	5067-6866	x8 8654 to 2x4 8643, 9402 SAS 1M	Two x4 SFF-8643 (mini-SAS HD)
05-60004-00	5067-6103	x8 8654 to 2x4 8654, 9402 1M	Two x4 SFF-8654 (SlimSAS)
05-60005-00	5067-6682	x8 8654 to 2xU.2 Direct, 1M	Two U.2 SFF-8639
05-60006-00	5067-7542	x8 8654 to 8xU.3 Direct, 1M	Eight U.3 SFF-8639
05-60007-00	5067-6869	x8 8654 to 1x8 8654, 9402 1M	One x8 SFF-8654 (SlimSAS)

**Table 10: Adapter Broadcom Cable Use Cases** 

MPN	Туре	Description	
05-60001-00	SAS/SATA and NVMe	Use for backplanes with x4 SFF-8612 connectors with pinouts that follow the SFF-9402 specification.	
05-60002-00	NVMe	Specialty cable that provides NVMe connections for SuperMicro Purley backplanes. This cable has white SFF-8643 connectors to indicate that it must connect to the white SFF-8643 connectors on the SuperMicro Purley backplanes.	
05-60003-00	SAS/SATA	Use for traditional SAS/SATA connections. Usually backplanes designed to support SAS/SATA only or are double plumbed for U.2 and SAS/SATA drives use SFF-8643 connectors.	
05-60004-00	SAS/SATA and NVMe	Use for backplanes with x4 SFF-8654 connectors with pinouts that follow the SFF-9402 specification.	
05-60005-00	NVMe	Enables direct connect from the adapter to a U.2 NVMe drive. Use for proof-of-concept type applications.	
05-60006-00	SAS/SATA and NVMe	Enables direct connect from the adapter to a U.3 NVMe or SAS/SATA drive. This cable does not send a PCIe REFCLK or PERST# to each drive connector; that is, the U.3 drive must support SRIS and not require PERST#. Use for proof-of-concept type applications.	
05-60007-00	SAS/SATA and NVMe	Use for backplanes with x8 SFF-8654 connectors with pinouts that follow the SFF-9402 specification.	

### **Backplane Connectors**

The SFF-8612 or SFF-8654 connectors are the preferred connectors to use for the NVMe backplane or multiprotocol backplanes, based on the SFF-TA-1001 universal bay definition.

Backplanes that use OCuLink connectors should follow the *PCI Express OCuLink Specification*. This pinout is also equivalent to the SFF-9402 specification recommendations. Verify the backplane connector pinout to make sure you use proper cabling to the NVMe drive. Refer to the *PCI Express OCuLink Specification* and the SFF-9402 specification for backplane NVMe connector pinout information.

# **External Adapter Connector Pinout**

External adapters support SAS and PCIe connections. The PCIe cable specification swaps lanes 0 and 1 compared to the SAS specification. For PCIe connections, this swap means the external pinout must place lanes 0 and 1 on the same pins as the JBOF. This swap does not impact SAS connections because lane ordering does not impact SAS designs.

For external PCIe JBOF connections, as defined by the *PCI Express External Cabling Specification*, the adapter does not connect REFCLK and PERST#. The adapter only supports an SRIS-capable endpoint, that is, no REFCLK. The JBOF handles the drive (connected to the JBOF switch) start-of-day reset, hot insertion, and clocking requirements.

The adapter expects a local (single master) two-wire bus connection to the cable or active module's EEPROM. A cable requires a local EEPROM on each end to identify cable properties, such as length, loss budget, ganging, and so on. The adapter supports no direct communication to the enclosure over two-wire. SES performs enclosure management.

The following table shows the cable pinout for the cable that Broadcom provides for the external adapter. See Cable Drawings and Pinouts for drawings and pinouts of each listed cable. Use the drawings if you source your own cables.

**Table 11: External Adapter Cable Pinout** 

Pin	Signal	Pin	Signal
A1	No Connect	C1	CMICLK
A2	CINT#	C2	CMIDAT
A3	GND	C3	GND
A4	PERp0, RX0+	C4	PETp0, TX0+
A5	PERn0, RX0-	C5	PETn0, TX0-
A6	GND	C6	GND
A7	PERp3, RX3+	C7	PETp3, TX3+
A8	PERn3, RX3-	C8	PETn3, TX3-
A9	GND	C9	GND
B1	PWR	D1	PWR
B2	CBLPRSNT#	D2	MGTPWR
B3	GND	D3	GND
B4	PERp1, RX1+	D4	PETp1, TX1+
B5	PERn1, RX1-	D5	PETn1, TX1-
B6	GND	D6	GND
B7	PERp2, RX2+	D7	PETp2, TX2+
B8	PERn2, RX2-	D8	PETn2, TX2-
B9	GND	D9	GND

Broadcom provides the following cable to use for external adapters. Use the MPN listed to order the cable from Broadcom. If you source your own cables, use the Broadcom part number from the following table, and the drawings and pinouts in Cable Drawings and Pinouts.

**Table 12: External Adapter Cable** 

MPN	Broadcom Cable Part No.	Cable Description	Backplane Connector
05-60009-00	5067-9643	G4/S4 x4 8644 to x4 8644, 3M	Two x4 SFF-8644

### **CacheVault Data Protection**

The MegaRAID Tri-Mode storage adapters support data retention by using NAND flash memory down on the adapter, backed up by a CacheVault<sup>™</sup> Power Module 05 (CVPM05).

The CVPM05 module is a super-capacitor pack that provides power for the backup of your data in case of host power loss or server failure. The CVPM05 module connects to the controller remotely by cable. The data is backed up to the NAND flash memory available on the MegaRAID storage adapter.

#### **NOTE**

If you do not use the remote mount board or clip included with the CacheVault kit, do not damage the CVPM05 module when mounting in the system. For more information on mounting the CVPM05 module, refer to CVPM02, CVPM05 Power Modules | CVFM04 Cache Module MegaRAID CacheVault Protection Products User Guide.

In the event of host power loss or server failure, any data available in the cache is offloaded to the onboard NAND memory. During this process, the CVPM05 power module powers the necessary components needed for offload.

#### **NOTE**

You cannot hot plug CVPM05 modules. Removing or inserting a CVPM05 module with the adapter powered on might damage the board and the super-capacitor functionality. To attach or remove a CVPM05 module from an adapter, you must fully power down the adapter before you attach the module to or remove the module from its mating connector.

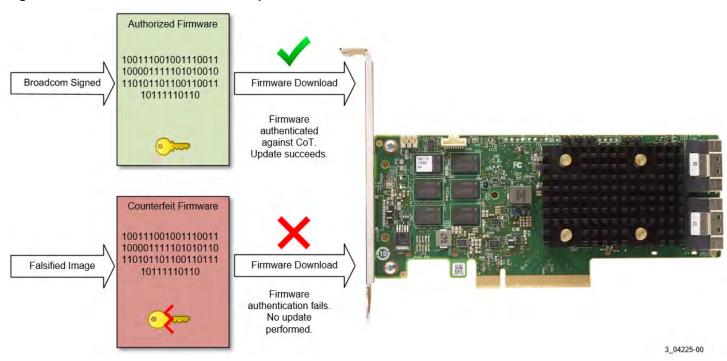
For more information on installation of the CVPM05 module, refer to the *CacheVault Power Module 05 Getting Started Guide* available at http://www.broadcom.com/support/download-search.

# **Adapter Security**

The adapter hardware secure boot security feature protects your system from malicious activity.

Hardware secure boot permits only authenticated firmware to execute on the adapter. The adapter boots from an internal boot ROM, which establishes the initial root of trust (RoT). Hardware secure boot authenticates and builds a chain of trust (CoT) with succeeding firmware images by using the RoT, meaning only authorized firmware executes on the adapter.

Figure 2: Authenticated Firmware Example



Hardware secure boot requires that each image be signed with a valid digital signature; otherwise, the image is considered invalid and does not execute. The adapter ships with a valid signed firmware image. All Broadcom-supplied firmware includes a valid digital signature; therefore, the hardware secure boot process is transparent unless the adapter encounters a counterfeit image. If the adapter downloads a counterfeit image, the image authentication fails and the download utility, such as StorCLI2, displays the appropriate failure messages. Contact Broadcom Technical Support for assistance.

# **Adapter Installation Instructions**

This chapter provides detailed instructions on how to install your adapter. Make sure to use the proper installation steps for your adapter:

- Adapter Installation Instructions: MegaRAID 9560-16i, MegaRAID 9560-8i, MegaRAID 9580-8i8e, MegaRAID 9540-8i, HBA 9500-16i, HBA 9500-8i, HBA 9500-16e, or HBA 9500-8e
- OCP Adapter Installation Instructions: MegaRAID 9562-16i or HBA 9502-16i

### **Adapter Installation Instructions**

Use the following steps to install any of the following adapters:

- MegaRAID 9560-16i
- MegaRAID 9560-8i
- MegaRAID 9580-8i8e
- MegaRAID 9540-8i
- HBA 9500-16i
- HBA 9500-8i
- HBA 9500-16e
- HBA 9500-8e

#### 1. Unpack your adapter.

Unpack and remove the adapter. Inspect the adapter for damage. If it appears damaged, contact Broadcom Technical Support.

#### **ATTENTION**

To avoid the risk of data loss, back up your data before you change your system configuration.

#### 2. Turn off the power to the system.

Turn off the power to the computer and disconnect the AC power cord. Remove the computer cover. Refer to the system documentation for instructions. Before you install the adapter, make sure that the computer is disconnected from the power and from any networks.



#### **CAUTION**

Disconnect the computer from the power supply and from any networks to which you will install the adapter, or you risk damaging the system or experiencing electrical shock.

- 3. **Review the adapter connectors.** See Broadcom MegaRAID and HBA Tri-Mode Storage Adapter Characteristics for descriptions of the adapters that show their connectors.
- 4. Check the mounting bracket on the adapter.

If required for your system, replace the full-profile mounting bracket that ships on the adapter with the low-profile bracket supplied. Complete the following steps to attach the low-profile bracket:

- a) Using a No. 1 Phillips screwdriver that is ESD safe, remove the two Phillips screws that connect the full-profile bracket to the board. Unscrew the two screws located at the top and bottom edges of the board. Avoid touching any board components with the screwdriver or the bracket.
- b) Remove the full-profile bracket. Do not damage the adapter.
- c) Place the adapter on top of the low-profile bracket. Position the bracket so that the screw holes in the tabs align with the openings in the board.
- d) Using a No. 1 Phillips torque screwdriver that is ESD safe, set to a maximum torque of 4.8 ±0.5 inch-pounds. Replace the two Phillips screws removed in Step a.

#### **ATTENTION**

Exceeding this torque specification can damage the board, connectors, or screws, and can void the warranty on the board.

#### **ATTENTION**

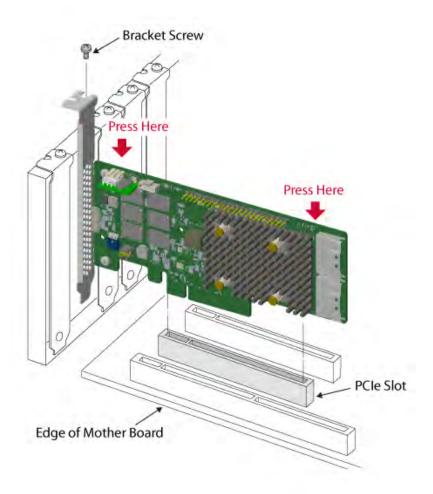
Damage caused to the board as a result of changing the bracket can void the warranty on the board. Adapters returned without a bracket mounted on the board will be sent back without return merchandise authorization (RMA) processing.

5. **Insert the adapter into an available PCIe slot.** Select a PCIe slot, and align the adapter's PCIe bus connector to the slot, as shown in the following figure. Press down gently, but firmly, to make sure that the adapter is seated correctly in the slot. Secure the bracket to the computer chassis with the bracket screw.

#### **NOTE**

Adapters with a x8 host interface can operate in x8 or x16 slots. However, some x16 PCle slots support only PCle graphics cards; an adapter installed in one of these slots will not function. Refer to the guide for your motherboard for information about the PCle slots.

Figure 3: Installing an Adapter in a PCle Slot



- 6. Configure and install the SAS, SATA, and PCIe (NVMe) devices in the host computer case.
  - Refer to documentation for the devices for any preinstallation configuration requirements.
- 7. **Connect the adapter to the devices.** Connect the appropriate cable with the connectors on one end to connect to the adapter and the appropriate connector on the other end to attach to the backplane connector.
  - The maximum cable length is 1 meter (39.37 in.). A single wide-port SAS or multilane PCIe (NVMe) device cannot connect to phys controlled by different SAS cores or PDBs. See Tri-Mode Storage Interface for more information.
- 8. **Provide the required airflow for the installed adapter.** See Operating and Nonoperating Conditions to find the adapter's cooling requirements.
- 9. **Turn on the power to the system.** Reinstall the computer cover, and reconnect the AC power cords. Make sure that the power is turned on to the storage devices before or at the same time that the power is turned on to the host computer. Turn on power to the host computer. If the computer is powered on before these devices, the devices might not be recognized.

During boot, a BIOS message appears. The firmware takes several seconds to initialize. The configuration utility prompt times out after several seconds. The second portion of the BIOS message shows the adapter controller number, firmware version, and cache SDRAM size. The numbering of the adapters follows the PCIe slot scanning order used by the host motherboard.

- 10. **Choose the correct storage profile.** Refer to the 12Gb/s MegaRAID Tri-Mode Software User Guide and LSI<sup>®</sup> Storage Authority Software User Guide for details about setting up your adapter.
- 11. **Install the operating system driver.** The adapters can operate under various operating systems. To operate under these operating systems, you must install the software drivers. The firmware and drivers are routinely updated and made available on the Support and Download center. Visit <a href="http://www.broadcom.com/support/download-search">http://www.broadcom.com/support/download-search</a>, and download the latest firmware and driver for the adapter.

The hardware installation of your adapter is complete.

### **OCP Adapter Installation Instructions**

Use the following steps to install any of the following adapters:

- MegaRAID 9562-16i
- HBA 9502-16i
- 1. Unpack your adapter.

Unpack and remove the adapter. Inspect the adapter for damage. If it appears damaged, contact Broadcom Technical Support.

#### **ATTENTION**

To avoid the risk of data loss, back up your data before you change your system configuration.

2. Turn off the power to the system.

Turn off the power to the computer and disconnect the AC power cord. Remove the computer cover. Refer to the system documentation for instructions. Before you install the adapter, make sure that the computer is disconnected from the power and from any networks.

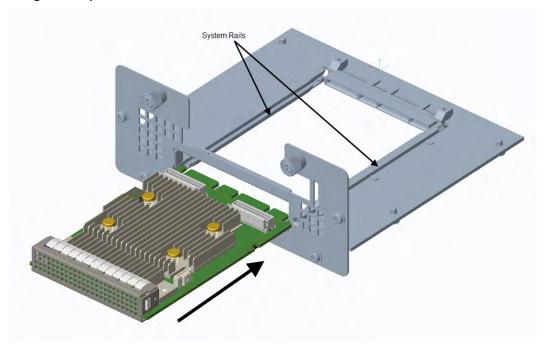


#### **CAUTION**

Disconnect the computer from the power supply and from any networks to which you will install the adapter, or you risk damaging the system or experiencing electrical shock.

3. **Insert the adapter into an available OCP 3.0 SFF slot.** Select an OCP slot, and align the adapter with the system rails and push the adapter into the slot, as shown in the following figure.

Figure 4: Installing an Adapter in an OCP Slot



- 4. **Secure the adapter.** Secured by the internal locking mechanism. A clicking sound is made when the adapter is secured into the slot. The internal locking mechanism depends on the server vendor. Adhere to the server vendor's instructions to engage the internal lock so the adapter is retained in the slot.
- 5. **Attach the cables.** Attach the cables to the storage device ports. When using the MegaRAID 9562-16i adapter, attach the SuperCap cable to the SuperCap cable latching connector.

### Removing the OCP Adapter

Use the following steps to remove any of following adapters from an OCP slot:

- MegaRAID 9562-16i
- HBA 9502-16i
- 1. Turn off the power to the system.

Turn off the power to the computer and disconnect the AC power cord. Remove the computer cover. Refer to the system documentation for instructions. Before you install the adapter, make sure that the computer is disconnected from the power and from any networks.



#### **CAUTION**

Disconnect the computer from the power supply and from any networks to which you will install the adapter, or you risk damaging the system or experiencing electrical shock.

- 2. **Unplug the cables.** Remove the SlimSAS cables. When using the MegaRAID 9562-16i adapter, remove the SuperCap cable.
- 3. **Remove the adapter from the OCP slot.** Adhere to the server vendor's instructions to disengage the internal lock. To remove the adapter from the OCP slot, carefully apply even pressure to the inside edges of the bracket.

#### NOTE

Do not use the vertical SlimSAS connectors or heatsink on the adapter for leverage.

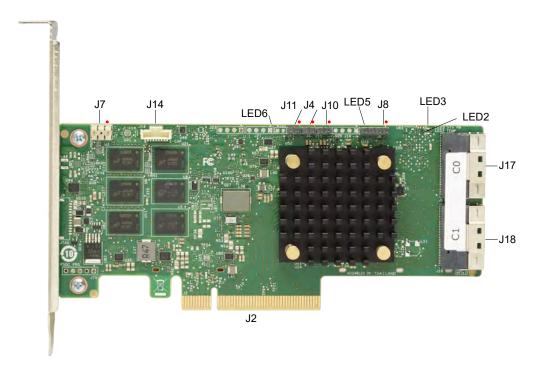
# **Broadcom MegaRAID and HBA Tri-Mode Storage Adapter Characteristics**

### MegaRAID 9560-16i Adapter – Connector and LED Designations

The adapter is a 155.52 ( $\pm 0.13$ ) mm  $\times$  68.77 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 5: Card Layout for the MegaRAID 9560-16i Tri-Mode Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 13: Headers and Connectors** 

Connector	Туре	Description
J2		The interface between the storage adapter and the host system.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the Intelligent Platform Management Interface (IPMI).
J4	Default serial boot ROM (SBR) header	2-pin connector. Reserved for Broadcom use.

Connector	Туре	Description	
J7	Advanced software options hardware key header	2-pin connector. Enables support for selected advanced features.	
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.	
J10	Global HDD activity LED header	2-pin connector.  Connects to an LED that indicates activity on the drives connected to the adapte	
J11	Global drive fault LED header	2-pin connector.  Connects to an LED that indicates whether a drive is in a fault condition.	
J14	CacheVault power module interface	9-pin connector.  Connects the adapter to a CacheVault power module.	
J17, J18	Storage interface connectors	Two SFF-8654 8-port internal connectors. Connect the adapter by cable to the storage devices.	

The following table describes the LEDs on the adapter.

**Table 14: LED Designations** 

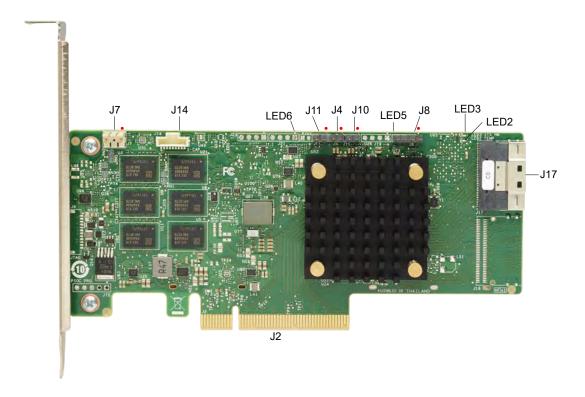
LED	Туре	Description
LED 2	Yellow controller over temperature	Stays on solid to indicate that the SAS3916 device temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED 3	Green system heartbeat	Indicates that the SAS3916 RoC ASIC is operating normally. This LED blinks at 1 Hz.
LED 5	Yellow supercap fault	Indicates that the CacheVault power module is in a fault state or is over the temperature threshold. This LED resides on the non-heat-sink side of the board.
LED 6	Green ONFI activity	Indicates when the ONFI is active for cache offload or recovery. This LED resides on the non-heat-sink side of the board.

# MegaRAID 9560-8i Adapter - Connector and LED Designations

The adapter is a 155.52 ( $\pm 0.13$ ) mm × 68.77 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 6: Card Layout for the MegaRAID 9560-8i Tri-Mode Storage Adapter



**Table 15: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J7	Advanced software options hardware key header	2-pin connector. Enables support for selected advanced features.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J10	Global HDD activity LED header	2-pin connector. Connects to an LED that indicates activity on the drives connected to the controller.
J11	Global drive fault LED header	2-pin connector. Connects to an LED that indicates whether a drive is in a fault condition.
J14	CacheVault power module interface	9-pin connector. Connects the adapter to a CacheVault power module.
J17	Storage interface connector	One SFF-8654 8-port internal connector. Connects the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

**Table 16: LED Designations** 

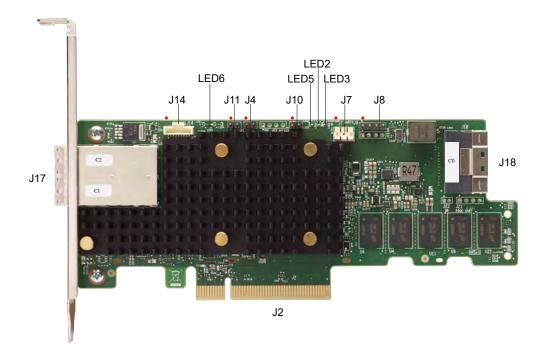
LED	Туре	Description
LED 2	Yellow controller over temperature	Stays on solid to indicate that the SAS3916 device temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED 3	Green system heartbeat	Indicates that the SAS3916 RoC ASIC is operating normally. This LED blinks at 1 Hz.
LED 5	Yellow supercap fault	Indicates that the CacheVault power module is in a fault state or is over the temperature threshold. This LED resides on the non-heat-sink side of the board.
LED 6	Green ONFI activity	Indicates when the ONFI is active for cache offload or recovery. This LED resides on the non-heat-sink side of the board.

# MegaRAID 9580-8i8e Adapter - Connector and LED Designations

The adapter is a 167.52 ( $\pm$  0.13) mm  $\times$  68.77 ( $\pm$  0.13) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 7: Card Layout for the MegaRAID 9580-8i8e Tri-Mode Storage Adapter



**Table 17: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J7	Advanced software options hardware key header	2-pin connector. Enables support for selected advanced features.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J10	Global HDD activity LED header	2-pin connector.  Connects to an LED that indicates activity on the drives connected to the controller.
J11	Global drive fault LED header	2-pin connector.  Connects to an LED that indicates whether a drive is in a fault condition.
J14	CacheVault power module interface	9-pin connector. Connects the adapter to a CacheVault power module.
J17	Storage interface connector	Two SFF-8644 4-port external connector. Connects the adapter by cable to the storage devices.
J18	Storage interface connector	One SFF-8654 8-port internal connector. Connects the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

**Table 18: LED Designations** 

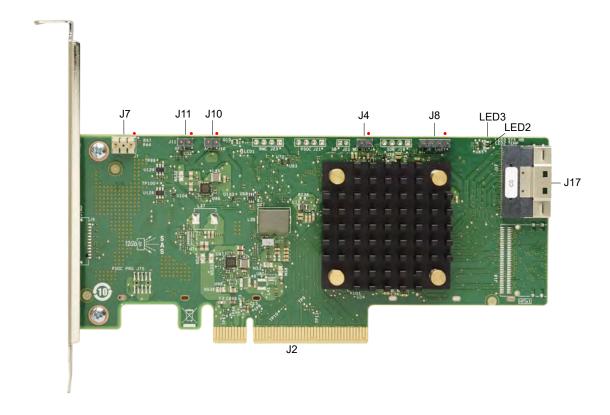
LED	Туре	Description
LED 2	Yellow controller over temperature	Stays on solid to indicate that the SAS3916 device temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED 3	Green system heartbeat	Indicates that the SAS3916 RoC ASIC is operating normally. This LED blinks at 1 Hz.
LED 5	Yellow supercap fault	Indicates that the CacheVault power module is in a fault state or is over the temperature threshold.
LED 6	Green ONFI activity	Indicates when the ONFI is active for cache offload or recovery.

# MegaRAID 9540-8i Adapter – Connector and LED Designations

The adapter is a 155.52 ( $\pm 0.13$ ) mm × 68.77 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 8: MegaRAID 9540-8i Tri-Mode Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 19: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J4	Default SBR header	2-pin connector. Reserved.
J7	Advanced software options hardware key header	2-pin connector. Enables support for selected advanced features.
J8	Onboard serial UART connector	4-pin connector. Reserved.
J10	Global HDD activity LED header	2-pin connector. Connects to an LED that indicates activity on the drives connected to the controller.
J11	Global drive fault LED header	2-pin connector.  Connects to an LED that indicates whether a drive is in a fault condition.

Connector	Туре	Description
J17	Storage interface connector	One SFF-8654 8-port internal connector.
		Connects the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

**Table 20: LED Designations** 

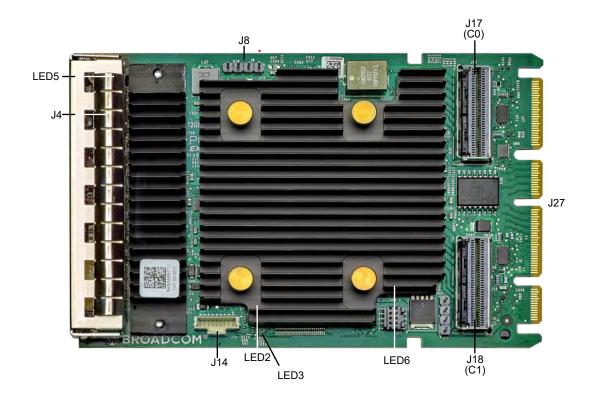
LED	Туре	Description
		Stays on solid to indicate that the SAS3808 IOC temperature sensor is over the temperature threshold. When the IOC is in the proper temperature range, this LED is off.
LED3	Green system heartbeat	Indicates that the SAS3808 IOC is operating normally.

# MegaRAID 9562-16i Adapter - Connector and LED Designations

The adapter is a 115.00 ( $\pm$  0.13) mm × 76.00 ( $\pm$  0.13) mm board. The component height on the top and bottom of the adapter complies with the OCP 3.0 NIC specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 9: Card Layout for the MegaRAID 9562-16i Tri-Mode Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 21: Headers and Connectors** 

Connector	Туре	Description
J4	Default serial boot ROM (SBR) header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J14	CacheVault power module interface	9-pin connector. Connects the adapter to a CacheVault power module.
J17, J18	Storage interface connectors	Two SFF-8654 8-port internal connectors. Connect the adapter by cable to the storage devices.
J27	Card PCIe edge connector	The interface between the storage adapter and the host system.

The following table describes the LEDs on the adapter.

**Table 22: LED Designations** 

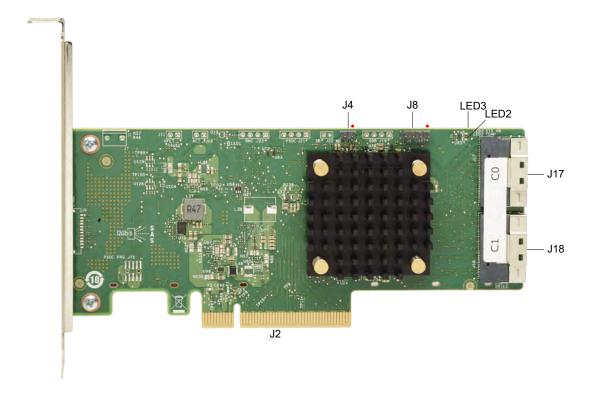
LED	Туре	Description
LED 2	Yellow controller over temperature	Stays on solid to indicate that the SAS3916 device temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED 3	Green system heartbeat	Indicates that the SAS3916 RoC ASIC is operating normally. This LED blinks at 1 Hz.
LED 5	Yellow supercap fault	Indicates that the CacheVault power module is in a fault state or is over the temperature threshold. This LED resides on the non-heat-sink side of the board.
LED 6	Green ONFI activity	Indicates when the ONFI is active for cache offload or recovery. This LED resides on the non-heat-sink side of the board.

# HBA 9500-16i Adapter - Connector and LED Designations

The adapter is a 155.52 ( $\pm 0.13$ ) mm  $\times$  68.77 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 10: Card Layout of the HBA 9500-16i Tri-Mode Storage Adapter



The following table describes the connectors on the adapter.

**Table 23: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system. With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J17, J18	Storage interface connectors	Two SFF-8654 8-port internal connectors. Connect the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

**Table 24: LED Designations** 

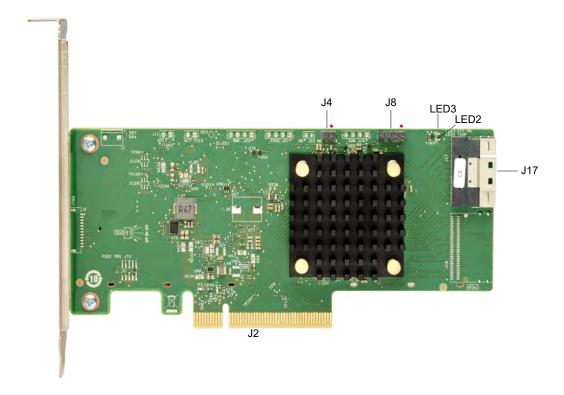
LED	Туре	Description
LED2	1	Stays on solid to indicate that the SAS3816 IOC temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED3	Green system heartbeat	Indicates that the SAS3816 IOC is operating normally.

# HBA 9500-8i Adapter – Connector and LED Designations

The adapter is a 155.52 ( $\pm 0.13$ ) mm × 68.77 ( $\pm 0.13$ ) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 11: Card Layout of the HBA SAS 9500-8i Tri-Mode Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 25: Headers and Connectors** 

Connector	Туре	Description
J2	Standard edge card connector	The interface between the storage adapter and the host system.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.
J17	Storage interface connector	One SFF-8654 8-port internal connector. Connects the adapter by cable to the storage devices.

The following table describes the LEDs on the adapter.

**Table 26: LED Designations** 

LED	Туре	Description
LED2	Yellow IOC over temperature	Stays on solid to indicate that the SAS3808 IOC temperature sensor is over the temperature threshold. When the IOC is in the proper temperature range, this LED is off.
LED3	Green system heartbeat	Indicates that the SAS3808 IOC is operating normally.

# **HBA 9500-16e Adapter – Connector and LED Designations**

The adapter is a 167.65 ( $\pm$  0.13) mm  $\times$  68.77 ( $\pm$  0.13) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 12: Card Layout of the HBA 9500-16e Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 27: Connectors** 

Connector	Туре	Description
J2	Standard board edge connector	PCIe x8 board edge connector.  With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J18	Storage interface connectors	Four SFF-8644 external connectors. Connect the adapter by cable to the storage devices.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.

The following table describes the LEDs on the adapter.

**Table 28: LED Designations** 

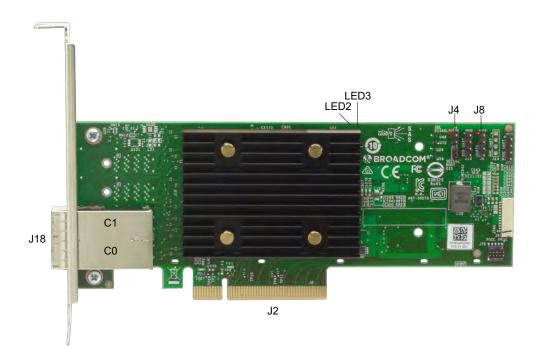
LED	Туре	Description
LED2	Yellow IOC over temperature	Stays on solid to indicate that the SAS3816 IOC temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off. This LED resides on the non-heat-sink side of the board.
LED3	Green system heartbeat	Indicates that the SAS3816 IOC is operating normally. This LED resides on the non-heat-sink side of the board.

# **HBA 9500-8e Adapter – Connector and LED Designations**

The adapter is a 167.65 ( $\pm$  0.13) mm  $\times$  68.77 ( $\pm$  0.13) mm board. The component height on the top and bottom of the adapter complies with the PCIe specification.

The following figure shows the connectors and LED locations on the adapter. A red circle near each header and connector identifies pin 1 in the figure.

Figure 13: Card Layout of the HBA 9500-8e Storage Adapter



The following table describes the headers and connectors on the adapter.

**Table 29: Headers and Connectors** 

Connector	Туре	Description
J2	Standard board edge connector	PCIe x8 board edge connector. With the PCIe interface, this connector provides power to the board and an I <sup>2</sup> C interface connected to the I <sup>2</sup> C bus for the IPMI.
J18	Storage interface connectors	Two SFF-8644 external connectors. Connect the adapter by cable to the storage devices.
J4	Default SBR header	2-pin connector. Reserved for Broadcom use.
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.

The following table describes the LEDs on the adapter.

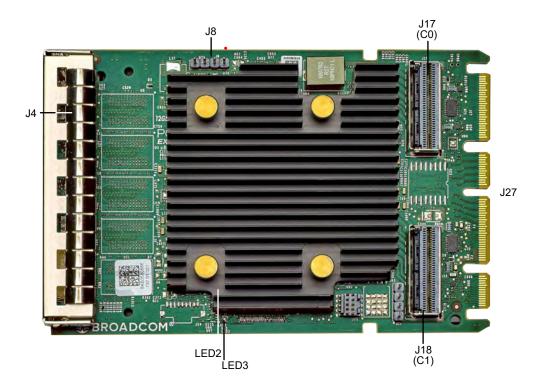
**Table 30: LED Designations** 

LED	Туре	Description
LED2	Yellow IOC over temperature	Stays on solid to indicate that the SAS3816 IOC temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off. This LED resides on the non-heat-sink side of the board.
LED3	Green system heartbeat	Indicates that the SAS3816 IOC is operating normally. This LED resides on the non-heat-sink side of the board.

# HBA 9502-16i Adapter - Connector and LED Designations

The adapter is a 115.00 ( $\pm$  0.13) mm  $\times$  76.00 ( $\pm$  0.13) mm board. The component height on the top and bottom of the adapter complies with the OCP 3.0 NIC specification.

Figure 14: Card Layout of the HBA 9502-16i Tri-Mode Storage Adapter



The following table describes the connectors on the adapter.

**Table 31: Headers and Connectors** 

Connector	Туре	Description	
J4	Default serial boot ROM (SBR) header	2-pin connector. Reserved for Broadcom use.	
J8	Onboard serial UART connector	4-pin connector. Reserved for Broadcom use.	
J17, J18	Storage interface connectors	Two SFF-8654 8-port internal connectors. Connect the adapter by cable to the storage devices.	
J27	Card PCIe edge connector	The interface between the storage adapter and the host system.	

The following table describes the LEDs on the adapter.

#### **Table 32: LED Designations**

LED	Туре	Description
LED2	Yellow controller over temperature	Stays on solid to indicate that the SAS3816 device temperature sensor is over the temperature threshold. When the device is in the proper temperature range, this LED is off.
LED3	Green system heartbeat	Indicates that the SAS3816 IOC ASIC is operating normally. This LED blinks at 1 Hz.

## Tri-Mode Storage Adapter Technical Specifications

## **Operating and Nonoperating Conditions**

The following table lists the operating (thermal and atmospheric) conditions and non-operating (such as storage and transit) environment for the storage adapters. The minimum airflow, measured as linear feet per minute (LFPM) at 55°C, must be met to avoid operating the controller's processor and board components above their maximum junction temperatures.

**Table 33: Operating and Non-operating Conditions** 

	Operating	Condition	Non-operating Environment		
Adapter	Relative Humidity Range (Non-condensing)	Minimum LFPM	Temperature Range <sup>a</sup>	Relative Humidity Range (Non-condensing)	Temperature Range
9560-16i	5% to 90%	200	0°C to +55°C	5% to 95%	–40°C to +70°C
9560-8i	5% to 90%	200	0°C to +55°C	5% to 95%	–40°C to +70°C
9580-8i8e	5% to 90%	200	0°C to +55°C	5% to 95%	–40°C to +70°C
9540-8i	5% to 90%	150	0°C to +55°C	5% to 95%	-40°C to +70°C
9500-16i	5% to 90%	150	0°C to +55°C	5% to 95%	-40°C to +70°C
9500-8i	5% to 90%	150	0°C to +55°C	5% to 95%	-40°C to +70°C
9500-16e	5% to 90%	150	0°C to +55°C	5% to 95%	-40°C to +70°C
9500-8e	5% to 90%	150	0°C to +55°C	5% to 95%	-40°C to +70°C
9562-16i	5% to 90%	200	0°C to +55°C	5% to 95%	-40°C to +70°C
9502-16i	5% to 90%	150	0°C to +55°C	5% to 95%	-40°C to +70°C

## Tri-Mode Storage Adapter Power Supply Requirements

All power is supplied to the tri-mode storage adapter through the PCle 3.3V rails  $(3.3V \pm 9\%)$  and the 12V rail  $(12V \pm 8\%)$ . Onboard switching regulator circuitry operates from the 3.3V rails and the 12V rail provides the necessary voltages.

Typical power is measured with maximum I/O traffic, typical silicon process material, and nominal voltages operating the card at an ambient temperature of 45°C with required airflow.

## MegaRAID Tri-Mode Storage Adapter Power Supply Requirements

The following table describes the typical power consumption of the MegaRAID adapters.

Table 34: MegaRAID Tri-Mode Storage Adapter Typical Power Consumption

Power Mode	Typical Power (W)				
rowel wode	9560-16i	9560-8i	9580-8i8e	9540-8i	
3.3V Supply	0.22	0.22	0.66	0.02	
+12V Supply	12.99	9.42	13.59	5.94	

a. For adapters with a CVPM, this range applies with or without the CVPM attached.

Power Mode	Typical Power (W)			
	9560-16i	9560-8i	9580-8i8e	9540-8i
Total Power	13.21	9.64	14.25	5.96

During the transparent learn cycle, the CacheVault power module consumes up to an additional 8W. The PCle 3.3V rail supplies the power for the learn cycle.

## **HBA Tri-Mode Storage Adapter Power Supply Requirements**

The following table describes the typical power consumption of the HBAs.

Table 35: HBA Tri-Mode Storage Adapter Typical Power Consumption

Power Mode	Typical Power (W)				
Fower Mode	9500-16i	9500-8i	9500-16e	9500-8e	
3.3V Supply	0.04	0.02	0.04	0.02	
+12V Supply	8.5	5.94	8.70	6.10	
Total Power	8.5	5.96	8.74	6.12	

## MegaRAID and HBA Tri-Mode OCP Adapter Power Supply Requirements

The following table describes the typical power consumption of the OCP adapters.

**Table 36: OCP Tri-Mode Storage Adapter Typical Power Consumption** 

Power Mode	Typical Power (W)		
rowel Mode	9562-16i	9502-16i	
3.3V Supply	0.3	0.1	
+12V Supply	13.5	8.9	
Total Power	13.8	9.0	

For the MegaRAID 9562-16i adapter, the CacheVault power module consumes up to an additional 8W during the transparent learn cycle. The PCIe 3.3V rail supplies the power for the learn cycle.

# Marks, Certifications, Compliance, and Safety Characteristics

# Marks, Certifications, and Compliance

The design and implementation of the adapters minimize electromagnetic emissions, susceptibility to radio frequency energy, and the effects of electrostatic discharge. The following adapters show the marks and certifications included in Table 38, Adapter Marks and Certifications.

**Table 37: Adapter Models** 

Adapter	Model Number
MegaRAID 9560-16i MegaRAID 9560-8i	50077
HBA 9500-16i HBA 9500-8i	2022 and forward: 50134 Previous years: 50077
MegaRAID 9540-8i	50134
MegaRAID 9580-8i8e	50076
HBA 9500-16e HBA 9500-8e	50075
MegaRAID 9562-16i HBA 9502-16i	50137

**Table 38: Adapter Marks and Certifications** 

Mark	Symbol	Description
Australia and New Zealand RCM		Meets the following standards:  • AS/NZS CISPR 32  • CISPR 32:2015, Class B  • AS/NZS CISPR 32:2015, Class B
Canada EMC	CANADA ICES-OO3 CLASS B CANADA NMB-003 CLASSE B CAN ICES-3 (B)/NMB-3 (B)	Meets the following standards:  ICES-003:2016 Issue 6: 2016, Class B  CAN/CSA CISPR 22-10  CISPR 22:2008
Europe (CE)	CE	Meets the following standards:  • EN 55022/EN 55024 EN 55032  • EN 55032:2015 +AC:2016, Class B  • EN 50022:2010 +AC:2011, Class B  • EN 55024:2010 +A1:2015

Mark	Symbol	Description					
Korea (RRL)	MSIP-REM-A8T-XXXXX	xxxxxx = model number Meets the KN32/KN35 testing requirements.					
Taiwan (BSMI)	D3B320 RoHS	Meets the following standards:  CNS 13438 CNS15663					
USA / Canada Safety (UL Listed)	LISTED ITE Accessory E257743  C  C  U  U  U  U  U  U  U  U  U  U  U	For use with UL listed ITE equipment only.  Meets the following standards:  UL 60950-1, 2nd Edition, 2014-10-14 (Information Technology Equipment - Safety -Part 1: General Requirements)  CAN/CSA C22.2 No. 60950-1-07, 2nd Edition, 2014-10 (Information Technology Equipment -Safety -Part 1: General Requirements)  UL 62368-1, Second Edition  CAN/CSA C22.2 No. 62368-1-14					
CB Scheme Safety	_	Meets the following standards:  • IEC 60950-1:2005 (Second edition) + Am 1:2009 + Am 2:2013  • EN 60950-1: 2006 + A11: 2009 + A1: 2010 + A12: 2011 + A2: 2013  • IEC 62368-1:2014 (Second edition)  • EN 62368-1:2014+A11: 2017					
Japan (VCCI)	VEI	Meets the following standards:  • V-3/2015.04, Class B  • V-4/2012.04  • VCCI-CISPR 32:2016  • CISPR 32:2015					
USA / Canada (FCC)	FC	Meets the following standards:  • 47 CFR FCC Part 15, Subpart B, Class B  • ANSI C63.4:2014  • CISPR 32:2008					

Mark	Symbol	Description					
Morocco (CMIM)	6	Meets the following standards:  • EN 55022/EN 55024 EN 55032  • EN 55032:2015 +AC:2016, Class B  • EN 50022:2010 +AC:2011, Class B  • EN 55024:2010 +A1:2015					
Country of Origin	Made in XXXX	XXXX indicates the country of origin.					

### **FCC Compliance**

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) this device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

#### NOTE

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

## **Safety Characteristics**

All tri-mode storage adapters meet or exceed the requirements of UL flammability rating 94 V0. Each bare board is also marked with the supplier name or trademark, type, and UL flammability rating. For the boards installed in a PCIe bus slot, all voltages are lower than the SELV 42.4V limit.

## VCCI - Japan

この装置は、クラスA機器です。この装置を住宅環境で使用すると電波妨害を引き起こすことがあります。この場合には使用者が適切な対策を講ずるよう要求されることがあります。 VCCI - A

This is a Class A product. In a domestic environment this product may cause radio interference in which case the user may be required to take adequate measures. VCCI—A.

# **Cable Drawings and Pinouts**

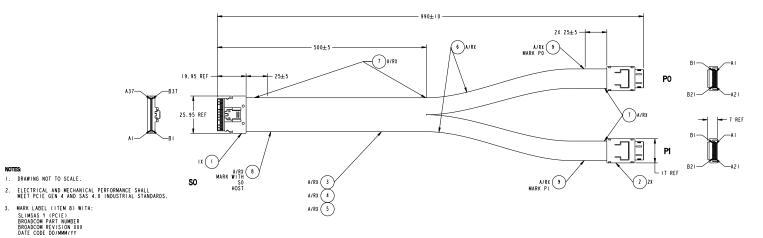
Use the cable drawings and pinouts in this appendix if your design requires you to design your own cables.

### Cable 05-60001-00

The following figure shows the drawing and pinout for Broadcom cable, 05-60001-00, a x8 SFF-8654 to 2 x4 SFF-8612 connection.

Figure 15: Cable 05-60001-00 Drawing and Pinout

REVISIONS								
REV	ECO	DESCRIPTION	DATE	APPROVED				
001		PRELIMINARY RELEASE	26FEB18					
002	175141	UPDATE WIRE GAUGE AND IMPEDANCE TOLERANCE ITEM 3. REVISE MARKING REQUIREMENTS NOTE 3	21NOV19					



SECONDARY MARKING WITH MANUFACTURER PART NUMBER AND REVISION ACCEPTABLE.

4. SEE CABLE WIRING TABLE AND RELATED NOTES.

9	LABEL	LABEL, 40XI4MM, WHITE, R2, HF	A/R	
8	LABEL	LABEL, 70X26MM, WHITE, R2, HF	A/R	SEE NOTE 3
7	TAPE	ACETATE TAPE: W=1 INCH	A/R	
6	SLEEVING	EXPANDO TUBE: OD=7MM, GREEN, VW-I, HF	A/R	
5	SLEEVING	EXPANDO TUBE: OD=10MM, GREEN, VW-I, HF	A/R	
4	WIRE	ULIO61, STRANDED	A/R	SEE NOTE 4
3	CABLE	SAS CABLE: UL20744, 28-32AWG, 92.5Ω ±10% DIFF, SAS4, PCIE GEN 4, VW-I, NATURAL, HF	A/R	
2	CONNECTOR	SFF-8612, 42P, STRAIGHT, ACTIVE LATCH, X4, SAS 4.0, PCIE GEN 4	2	SEE NOTE 4
	CONNECTOR	SFF-8654, 74P, STRAIGHT, X8, SAS 4.0, PCIE GEN 4	1	SEE NOTE 4
ITEM	CALLOUT	DESCRIPTION	QUANTITY	NOTES

APPROVALS	DATE	UNLESS OTHERWISE DIMENSIONS ARE IN			A BROA		л•		
DRAWN ST	06FEB18	TOLERANCES: X,X ± 0,2 MM					11		
APPROVED BY	26FEB18	X.XX ± 0.15 M	M M	TITLE	CABLE, SFF-8654 X8 TO 2X				
Company Confidential © Copyright All Rights Reserved.Any copy is on		ANGLES ± 1°			SFF-8612 )	(4, PCIE,	IM		
The possessor is responsible for a document's revision is current, possessor is responsible for re documents from their poin	Moréovér, lhe Moving absolute	1.500	⊕€	S12tt D	5067-6865	002	SHEET		

CONNECTOR PIN/PAD CONNECTOR SIGNAL WIRE/CABLE NOTES

| CONNECTOR SIGNAL | CONNECTOR | PIN/PAD | NC | SO | HOST | A19 |

	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
NC	\$0_			P0	BI	NC		
GND	HOST	Al	-	BACKPLANE	B2	GND	ITEM 3	
R x 0 +		A2	Н—		B3	T x 0 +	ITEM 3	
R x 0 -		A3	H		84	T x 0 -	ITEM 3	
GND		A4	HX—		B5	GND	ITEM 3	
RxI+		A5	Н—		B6	TxI+	ITEM 3	
RxI-		A6	H <i>)</i> –		87	TxI-	ITEM 3	
GND		A7	$\vdash$		B8	GND	ITEM 3	
SB7A		A8				NC		NOTE 2
SB4A		A9	<del> </del>		BIO	CWAKE#	ITEM 4	NOTE I
SB3A		AI0	-		B9	CBL_ID	ITEM 4	NOTE I
SB3A		A10	$\vdash_{\wedge}$		BII	GND	ITEM 3	
SBA+		ALI	Н—		B12	REFCLK+	ITEM 3	
SBA-		A12	₩/-		B13	REFCLK-	ITEM 3	
GND		A13	1 <del>X</del> —		814	GND	ITEM 3	
Rx2+		A14	Н—		B15	Tx2+	ITEM 3	
Rx2-		A15	H /		B16	T x 2 -	ITEM 3	
GND		A16	ΙХ—		B17	GND	ITEM 3	
Rx3+		A17	Н—		B18	Tx3+	ITEM 3	
Rx3-		A18	₩		B19	Tx3-	ITEM 3	
GND		A19	V_		B20	GND	ITEM 3	
NC					B21	NC		
NC			1		Al	NC		
GND		ВІ	<u> </u>		A2	GND	ITEM 3	
Tx0+		B2	Н—		A3	R x 0 +	ITEM 3	
Tx0-		B3	H		A4	R x 0 -	ITEM 3	
GND		B4	₩—		A5	GND	ITEM 3	
Tx1+		B5	Н—		A 6	RxI+	ITEM 3	
Tx1-		B6	₩-		A7	RxI-	ITEM 3	
GND		87	$\vdash$		A8	GND	ITEM 3	
SBOA		B8	<del>                                     </del>		A9	SCL	ITEM 4	NOTE I
SBIA		B9	<u> </u>		A10	SDA	ITEM 4	NOTE I
SB2A		B10	-		All	GND	ITEM 3	
SB5A		BII	H+		A12	PERST#	ITEM 3	
SBGA		B12	Н—		A13	D_INPL#	ITEM 3	
GND		B13	HX—		A14	GND	ITEM 3	
Tx2+		B14	Н—		A15	RX2+	ITEM 3	
Tx2-		B15	₩-		A16	R x 2 -	ITEM 3	
GND		B16	HX—		A17	GND	ITEM 3	
Tx3+		B17	Н—		A18	R x 3+	ITEM 3	
Tx3-		B18	₩		A19	R x 3 -	ITEM 3	
GND		B19	Ι		A20	GND	ITEM 3	
NC					A21	NC		

CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
NC	\$0			PI	BI	NC		
GND	HOST	A 1 9	-	BACKPLANE	B2	GND	ITEM 3	
R x 4+	1	A20	144		B3	Tx0+	ITEM 3	
R x 4 -	1	A21	11 —		B4	Tx0-	ITEM 3	
GND	1	A22	1-X-	-	B5	GND	ITEM 3	
R x 5+	1	A23	₩.		B6	Tx1+	ITEM 3	
R x 5 -	1	A24	11.		B7	Tx1-	ITEM 3	
GND	1	A25	<u> </u>		B8	GND	ITEM 3	
SB7B	1	A26	1			NC		NOTE 3
SB4B	1	A27	1—		B10	CWAKE#	ITEM 4	NOTE I
SB3B	i	A28	ऻ		B9	CBL_ID	ITEM 4	NOTE I
SB3B	1	A28	<b>-</b> _		BII	GND	ITEM 3	
SBB+	1	A29	1//		B12	REFCLK+	ITEM 3	
SBB-	i	A30	Ш_	1	B13	REFCLK-	ITEM 3	
GND	1	A31	₩.		B14	GND	ITEM 3	
Rx6+	1	A32	1//		B15	Tx2+	ITEM 3	
Rx6-	1	A33	Ш		B16	Tx2-	ITEM 3	
GND	1	A34	₩.		B17	GND	ITEM 3	
Rx7+	1	A35	$\square$		B18	Tx3+	ITEM 3	
R x 7 -	1	A36	Ш		B19	Tx3-	ITEM 3	
GND	1	A37	V		B20	GND	ITEM 3	
NC NC	1		1		B21	NC NC	TIEM 3	
NC NC	1		1		AI	NC NC		
GND	1	B19			A2	GND	ITEM 3	
Tx4+	1	B20	Δ		A3	RxO+	ITEM 3	
Tx4-	-	B21	П		A4	Rx0-	ITEM 3	
GND	-	B22	W		A4 A5	GND	ITEM 3	
Tx5+	-	B23	$\Omega$			Rx I+	ITEM 3	
Tx5-		B24	П	1	A6 A7	RxI-	ITEM 3	
GND	-	B25	$\Pi$		A8	GND	ITEM 3	
			-				ITEM 3	NOTE :
SBOB	-	B26	-		A9 A10	SCL		NOTE I
SB1B SB2B		B27 B28		1		SDA	ITEM 4	NOTE I
			$\Lambda$	1	AII	GND		
SB5B		B29	H -		A12	PERST#	ITEM 3	
SB6B	1	B30	11/		A13	D_INPL#	ITEM 3	
GND		B31	$\times$		AI4	GND	ITEM 3	
Tx6+		B32	H -	1	A15	RX2+	ITEM 3	
T x 6 -		B33	11		A16	Rx2-	ITEM 3	
GND	1	B34	+X $-$	1	A17	GND	ITEM 3	
T x 7+	1	B35	Ш	1	A18	Rx3+	ITEM 3	
T x 7 -	1	B36	Ш	1	A19	Rx3-	ITEM 3	
GND	1	B37	$\vdash$	1	A20	GND	ITEM 3	
NC					A21	NC		

- I. END TO END RESISTANCE OF INDICATED CONNECTION SHALL BE 750 MILLIOHM MAX.
- 2. SHORT PIN/PAD SO-A8 TO SO-B9. NO DIRECT CONNECT SO-A8 TO CONNECTOR PO.
- 3. SHORT PIN/PAD SO-A26 TO SO-B27. NO DIRECT CONNECT SO-A26 TO CONNECTOR PI.

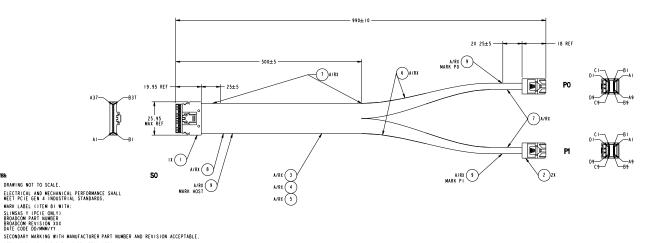
95xx-MR-HBA-Tri-Mode-UG111 Broadcom

## Cable 05-60002-00

The following figure shows the drawing and pinout for Broadcom cable, 05-60002-00, a x8 SFF-8654 to 2 x4 SFF-8643 connection. Use this cable for NVMe connections on SuperMicro Purley backplanes.

Figure 16: Cable 05-60002-00 Drawing and Pinout





4. SEE CABLE WIRING TABLE AND RELATED NOTES.

9	LABEL	LABEL, 40XI4MM, WHITE, R2, HF	A/R	
8	LABEL	LABEL, 70x26MM, WHITE, R2, HF	A/R	SEE NOTE 3
7	TAPE	ACETATE TAPE: W:I INCH	A/R	
6	SLEEVING	EXPANDO TUBE: OD=7MM, GREEN, VW-I, HF	A/R	
5	SLEEVING	EXPANDO TUBE: OD=10MM, GREEN, VW-1, HF	A/R	
4	WIRE	UL1061, STRANDED	A/R	SEE NOTE 4
3	CABLE	SAS CABLE: UL20744, 28-32AMG, 92.5Ω ±10% DIFF, PCIE GEN 4, VW-1, NATURAL, HF	A/R	
2	CONNECTOR	SFF-8643, 36P, STRAIGHT, X4, WHITE, SHORT, PCIE GEN 4	2	SEE NOTE 4
- 1	CONNECTOR	SFF-8654, 74P, STRAIGHT, X8, SAS 4.0, PCIE GEN 4	- 1	SEE NOTE 4
LTEM	CALLOUT	DESCRIPTION	OHANTITY	NOTES

APPROVALS	DATE	UNLESS OTHERWISE			A BROA		√I•
COARS SY	05FEB18	TOLERANCES: X.X ± 0.2 MM					~U
APPROVED BY	09FEB18	X.XX ± 0.15 X.XXX ± 0.05	MM	TITLE	CABLE, SFF-	8654 X8 T	2 X
Company Confidential O Copyright All Rights Reserved. Any copy is an	unconfrolled coor.	ANGLES ± 1°	•		SFF-8643	X4 (W),	I M
The passessor is responsible for a document's revision is current, passessor is responsible for re documents from their nois	Meresier, the Morine obsolete	1.500	⊕€	SIM D	5067-6862	003	SHEET

	(HOST)	-		CONNECTOR (TARGET)	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	CONNECTOR SIGNAL	CONNECTOR (HOST)	PIN/PAD		CONNECTOR (TARGET)	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE
GND	S0	Al	$\overline{}$	P0	D3	GND	ITEM 3	GND	S0	AI9	Λ	PI	D3	GND	ITEM 3
Rx0+		A2	ш		D4	Tx0+	ITEM 3	Rx4+	1	A20	+		D4	Tx0+	ITEM 3
R×0-		A3	HH		D5	Tx0-	ITEM 3	R x 4 -	1	A21	+		D5	Tx0-	ITEM 3
GND		A4	H		C3/D6	GND	ITEM 3, NOTE 2	GND		A22	Х		C3/D6	GND	ITEM 3, NOTE 2
RxI+		A5	н		C4	TxI+	ITEM 3	Rx5+		A23	H		C4	Tx1+	ITEM 3
RxI-		A6	ΗН		C 5	Tri-	ITEM 3	Rx5-	1	A24	+		C5	Tx1-	ITEM 3
GND		A7	$\vdash$		C6	GND	ITEM 3	GND		A25	V		C6	GND	ITEM 3
SBTA		A8	]			NC	NOTE 3	SB 7B		A26				NC	NOTE 5
SB4A		A9	1 1			NC		SB4B	1	A27				NC	
SB3A		A10	$\vdash_{\wedge}\vdash$		A3	GND	ITEM 3	SB3B		A28	$\Lambda$		A3	GND	ITEM 3
SBA+		ALI	н		A2	REFCLK+	ITEM 3	SBB+		A29	H		A2	REFCLK+	ITEM 3
SBA-		A12	HH		Al	REFCLK-	ITEM 3	SBB-	1	A30	$\cup$		Al	REFCLK-	ITEM 3
GND		A13	H		D6	GND	ITEM 3	GND	]	A31	Х		D6	GND	ITEM 3
Rx2+		A14	н		D7	Tx2+	ITEM 3	Rx6+		A32	H		D7	Tx2+	ITEM 3
Rx2-		A15	н		D8	Tx2·	ITEM 3	R×6-		A33	$\cup$		D8	Tx2-	ITEM 3
GND		A16	H		D9/C6	GND	ITEM 3, NOTE 2	GND		A34	Х		D9/C6	GND	ITEM 3, NOTE 2
Rx3+		A17	н		C7	Tx3+	ITEM 3	Rx7+		A35	H		C7	Tx3+	ITEM 3
Rx3-		A18	н		C8	Tx3-	ITEM 3	R x 7 -		A36	$\cup$		C8	Tx3-	ITEM 3
GND		A19	$\vdash$ $\vdash$		C9	GND	ITEM 3	GND	1	A37	V		C 9	GND	ITEM 3
GND		ВІ	$\vdash_{\wedge}$		B3	GND	ITEM 3	GND		B19	$\wedge$		B3	GND	ITEM 3
Tx0+		B2	н		84	Rx0+	ITEM 3	Tx4+		B20	H		B4	R×0+	ITEM 3
Tx0-		B3	НН		B5	R×0-	ITEM 3	Tx4-		B21	$\cup$		B5	R×0-	ITEM 3
GND		B4	ж		B6/A3	GND	ITEM 3, NOTE 2	GND		B22	Х		B6/A3	GND	ITEM 3, NOTE 2
TxI+		B5	н		A4	RxI+	ITEM 3	Tx5+		B23	H		A4	RxI+	ITEM 3
Tx1-		B6	н		A5	RxI-	ITEM 3	Tx5-		B24	$\cup$		A5	RxI-	ITEM 3
GND		B7	$\vdash$ $\vdash$		A6	GND	ITEM 3	GND		B25	V		A6	GND	ITEM 3
SBOA		B8	$\vdash$		DI	2W_CLK	ITEM 4, NOTE I	SBOB		B26			DI	2W_CLK	ITEM 4, NOTE I
SBIA		В9	$\vdash$		D2	2W_SDA	ITEM 4, NOTE I	SBIB		B27			D2	2W_SDA	ITEM 4, NOTE I
SB2A		BIO	]			NC	SHORT TO GND	SB2B		B28				NC	SHORT TO GND
SB5A		BII	$\vdash$		82	PERST#	ITEM 4, NOTE I	SB 5B		B29			B2	PERST#	ITEM 4, NOTE I
SB6A		B12				NC	SHORT TO GND	SB6B		B30				NC	SHORT TO GND
GND		B13	$\vdash$		B6	GND	ITEM 3	GND		B31	Λ		B6	GND	ITEM 3
Tx2+		B14	н		87	RX2+	ITEM 3	Tx6+		B32	HH		B7	RX2+	ITEM 3
Tx2-		B15	н		B8	Rx2-	ITEM 3	Tx6-		B33	$\cup$		B8	Rx2-	ITEM 3
GND		B16	Ж		B9/A6	GND	ITEM 3, NOTE 2	GND		B34	Х		B9/A6	GND	ITEM 3, NOTE 2
Tx3+		B17	ΗН		A7	Rx3+	ITEM 3	Tx7+	]	B35	HH		A7	Rx3+	ITEM 3
Tx3-		B18	HH		A8	Rx3-	ITEM 3	Tx7-	]	B36	ш		A8	R×3-	ITEM 3
GND		B19	$\vdash \vdash \vdash$		A9	GND	ITEM 3	GND	]	B37	$\vdash$		A 9	GND	ITEM 3
					CI	NC			]				CI	NC	
					C2	NC			]				C2	NC	
					BI	NC							BI	NC	

- TESE

  END TO END RESISTANCE OF INDICATED CONNECTION SHALL BE 750 MILLIOHM MAX.

  CONNECT SHIELD OF DIFFERENTIAL PAIR TO INDICATED PIN/PAD AND SHORT TO SECOND INDICATED PIN/PAD.

  SHORT PIN/PAD S0-AB TO S0-BB. NO DIRECT CONNECT S0-AB TO CONNECTOR PD.

  DELETED

  SHORT PIN/PAD S0-A26 TO S0-B27. NO DIRECT CONNECT S0-A26 TO CONNECTOR PI.

  DELETED

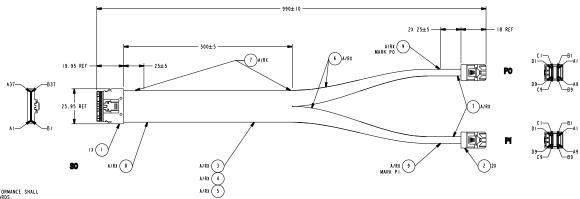
95xx-MR-HBA-Tri-Mode-UG111 Broadcom

## Cable 05-60003-00

The following figure shows the drawing and pinout for Broadcom cable, 05-60003-00, a x8 SFF-8654 to 2 x4 SFF-8643 connection.

Figure 17: Cable 05-60003-00 Drawing and Pinout

	REVISIONS									
REV	ECO	DESCRIPTION	DATE	APPROVED						
001		PRELIMINARY RELEASE	28FEB18							
002	175141	UPDATE WIRE GAUGE AND IMPEDANCE TOLERANCE ITEM 3. REVISE MARKING REQUIREMENTS NOTE 3.	2   NOV   9							



DAANING NOT 10 SCALE. ELECTRICAL ADD MECHANICAL PERFORMANCE SHALL MEET SAS 4.0 INDUSTRIAL STANDARDS.

MARK LABEL (ITEM 8) WITHS SLIMSAS Y (SFF-9402 SAS)
BROADCOM PART NUMBER BROADCOM REVISION XXX

DATE COME DOWNMANTY

DRAWING NOT TO SCALE.

SECONDARY MARKING WITH MANUFACTURER PART NUMBER AND REVISION ACCEPTABLE.

4. SEE CABLE WIRING TABLE AND RELATED NOTES.

9	LABEL	LABEL, 40XI4MM, WHITE, R2, HF	A/R	
8	LABEL	LABEL, 70X26MM, WHITE, R2, HF	A/R	SEE NOTE 3
7	TAPE	ACETATE TAPE: W=1 INCH	A/R	
6	SLEEVING	EXPANDO TUBE: OD=7MM, BLACK, VW-I, HF	A/R	
5	SLEEVING	EXPANDO TUBE: OD:IOMM, BLACK, VW-I, HF	A/R	
4	WIRE	UL1061, STRANDED	A/R	SEE NOTE 4
3	CABLE	SAS CABLE: UL20744, 28-32AWG, 92.5 $\Omega$ $\pm$ 10% DIFF, PCIE GEN 4, VW-1, NATURAL, HF	A/R	
2	CONNECTOR	SFF-8643, 36P, STRAIGHT, X4, BLACK, SHORT, SAS 4.0	2	SEE NOTE 4
- 1	CONNECTOR	SFF-8654, 74P, STRAIGHT, X8, SAS 4.0, PCIE GEN 4	- 1	SEE NOTE 4
ITEM	CALLOUT	DESCRIPTION	QUANTITY	NOTES

APPROVALS	DATE 15FEB18	UNLESS OTHERWISE DIMENSIONS ARE II			A) BRO	ADCOR	<b>1</b> °
APPROVED BY	28FEB18	X,X ± 0.2 MN X.XX ± 0.15 X.XXX ± 0.05	MM	CABLE, SFF-8654 X8 TO 2X			
Company Confidential & Copyright All Rights Reserved, Any copy is an The possessor is responsible for document's revision is current possessor is responsible for re	scale 1,500	⊕€	sin D	SFF-8 006 NO. 5067-6866	643 X4, IM	SHEET	

CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	Al		P0	D3	GND	ITEM 3	
Rx0+		A2	1/ 1		D4	Tx0+	ITEM 3	
Rx0-		A3	₩.	-	D5	Tx0-	ITEM 3	
GND		A4	1-X-		C3/D6	GND	ITEM 3	NOTE 2
RxI+		A5	1//		C4	Tx1+	ITEM 3	
RxI-	1	A6	H		C 5	Tx1-	ITEM 3	
GND	1	A7	1ν_		C6	GND	ITEM 3	
SB7A		A8	1—	-	A2	SB7	ITEM 4	NOTE I
SB4A		A9	-		C2	SB4	ITEM 4	NOTE I
SB3A		AIO	1—		B2	SB3	ITEM 4	NOTE I
SBA+		All	1			NC		
SBA-		A12	1			NC		
GND		A13	-		D6	GND	ITEM 3	
Rx2+		A14	₩		D7	Tx2+	ITEM 3	
Rx2-		A15	14.		D8	Tx2-	ITEM 3	
GND		A16	1 <del>.X</del> —		D9/C6	GND	ITEM 3	NOTE 2
Rx3+		A17	141		C7	Tx3+	ITEM 3	
Rx3-		81A	11		C8	Tx3-	ITEM 3	
GND		A19	$\mathcal{V}$		C 9	GND	ITEM 3	
GND		BI	1_		B3	GND	ITEM 3	
Tx0+		B2	1//		B4	R×0+	ITEM 3	
Tx0-		B3	1₩		B5	Rx0-	ITEM 3	
GND		84	1 <del>.X</del> —		B6/A3	GND	ITEM 3	NOTE 2
Tx1+	1	B5	141—		A4	RxI+	ITEM 3	
Tx1-		B6	11.		A5	RxI-	ITEM 3	
GND		B7	1_		A6	GND	ITEM 3	
SBOA		B8	1—		Al	SB0	ITEM 4	NOTE I
SBIA		89	1—		ВІ	SBI	ITEM 4	NOTE I
SB2A	1	B10	1—		CI	SB2	ITEM 4	NOTE I
SB5A	1	BII	1—		D2	SB5	ITEM 4	NOTE I
SB6A		B12	1—	-	DI	SB6	ITEM 4	NOTE I
GND		B13	-		B6	GND	ITEM 3	
Tx2+	1	B14	1/ 1	-	B7	RX2+	ITEM 3	
Tx2-		B15	₩—		B8	Rx2-	ITEM 3	
GND	1	B16	1-X-	-	B9/A6	GND	ITEM 3	NOTE 2
Tx3+	1	B17	₩-	-	A7	Rx3+	ITEM 3	
Tx3-	1	B18	111		A8	Rx3-	ITEM 3	
GND	1	B19	1ν_	-	A9	GND	ITEM 3	

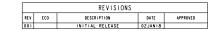
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	A19		PI	D3	GND	ITEM 3	
Rx4+	1	A20	144-	ł	D4	Tx0+	ITEM 3	
R x 4 -	1	A21	₩	ł	D5	Tx0-	ITEM 3	
GND		A22	1 <del>.X</del> —	ł	C3/D6	GND	ITEM 3	NOTE 2
Rx5+	1	A23	₩-	ł	C4	TxI+	ITEM 3	
Rx5-		A24	1		C5	Tx1-	ITEM 3	
GND	1	A25	$\mathcal{V}$	ł	C6	GND	ITEM 3	
SB7B		A26	-	1	A2	\$87	ITEM 4	NOTE I
SB4B		A27	$\vdash$	ł	C2	SB4	ITEM 4	NOTE I
SB3B	1	A28	$\vdash$	ł	B2	SB3	ITEM 4	NOTE I
SBB+		A29	]			NC		
SBB -		A30	1			NC		
GND		A31	-	1	D6	GND	ITEM 3	
Rx6+		A32	ж	1	D7	Tx2+	ITEM 3	
Rx6-	1	A33	₩	ł	D8	Tx2-	ITEM 3	
GND		A34	1 <del>.X</del> —	1	D9/C6	GND	ITEM 3	NOTE 2
R x 7+		A35	₩-	ł	C7	Tx3+	ITEM 3	
R x 7 -		A36	₩	ł	C8	Tx3-	ITEM 3	
GND		A37	<u> </u>		C 9	GND	ITEM 3	
GND	1	B19	-	ł	B3	GND	ITEM 3	
Tx4+	1	B20	₩-	1	B4	R x 0+	ITEM 3	
Tx4-		B21	1++-	ł	B5	R x 0 -	ITEM 3	
GND		B22	1 <del>.X</del> —	ł	B6/A3	GND	ITEM 3	NOTE 2
Tx5+		B23	ж		A4	RxI+	ITEM 3	
Tx5-	1	B24	₩	ł	A5	RxI-	ITEM 3	
GND		B25	$\vdash$	1	A6	GND	ITEM 3	
SBOB		B26	-	ł	Al	SB0	ITEM 4	NOTE I
SBIB		B27	-	-	ВІ	SBI	ITEM 4	NOTE I
SB2B		B28	_		CI	SB2	ITEM 4	NOTE I
SB5B	1	B29	$\vdash$	ł	D2	SB5	ITEM 4	NOTE I
SB6B		B30	-	1	DI	\$86	ITEM 4	NOTE I
GND		B31	-		B6	GND	ITEM 3	
Tx6+		B32	Н-		87	RX2+	ITEM 3	
Tx6-	1	B33	₩		B8	Rx2-	ITEM 3	
GND	1	B34	1 <del>.X</del> —	ł	B9/A6	GND	ITEM 3	NOTE 2
T x 7+	1	B35	₩-		A7	Rx3+	ITEM 3	
T x 7 -		B36	₩		A8	Rx3-	ITEM 3	
GND		B37	$\vdash$		A9	GND	ITEM 3	

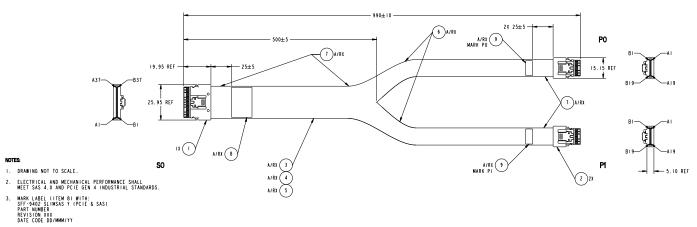
I. END TO END RESISTANCE OF INDICATED CONNECTION SHALL BE 750 MILLIOHM MAX.
2. CONNECT SHIELD OF DIFFERENTIAL PAIR TO INDICATED PIN/PAD AND SHORT TO SECOND INDICATED PIN/PAD.

## Cable 05-60004-00

The following figure shows the drawing and pinout for Broadcom cable, 05-60004-00, a x8 SFF-8654 to 2 x4 SFF-8654 connection.

Figure 18: Cable 05-60004-00 Drawing and Pinout





4. SEE CABLE WIRING TABLE.

9	LABEL	LABEL, 40XI4MM, WHITE, R2, HF	A/R	
8	LABEL	LABEL, 70x26MM, WHITE, R2, HF	A/R	SEE NOTE 3
7	TAPE	ACETATE TAPE: W=1 INCH	A/R	
6	SLEEVING	EXPANDO TUBE: OD=7MM, GREEN, VW-I, HF	A/R	
5	SLEEVING	EXPANDO TUBE: OD=10MM, GREEN, VW-I, HF	A/R	
4	WIRE	UL1061, 32AWG, STRANDED	A/R	
3	CABLE	SAS CABLE: UL20744, 32AWG, 85OHM DIFF, SAS4, PCIE GEN 4, VW-I, NATURAL, HF	A/R	
2	CONNECTOR	SFF-8654, 38P, STRAIGHT, X4, SAS 4.0, PCIE GEN 4	2	SEE NOTE 4
- 1	CONNECTOR	SFF-8654, 74P, STRAIGHT, X8, SAS 4.0, PCIE GEN 4		SEE NOTE 4
ITEM	CALLOUT	DESCRIPTION	QUANTITY	NOTES

APPROVALS	DATE	UNLESS OTHERWISE DIMENSIONS ARE IN	SPECIFIED:		A BROA		a•
DRAFE SY	27DEC   7	TOLERANCES: X.X ± 0.2 MM					٦ L
APPROVED BY	02JAN18	X.XX ± 0.15 M		TITLE	2 X		
Company Confidential © Copyright All Rights Reserved.Amy copy is an	ANGLES ± 1°		SFF-8654 X4, IM				
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CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE
GND	S0	Al	$\overline{}$	P0	BI	GND	ITEM 3
R x 0+	1	A2	₩-	-	B2	T x 0+	ITEM 3
R x 0 -	1	A3	₩		B3	Tx0-	ITEM 3
GND		A4	ŀX—	-	B4	GND	ITEM 3
RxI+	1	A5	₩-		B5	TxI+	ITEM 3
RxI-	1	A 6	1	-	B6	Tx1-	ITEM 3
GND	1	A7	₩_	-	87	GND	ITEM 3
SB7A	1	A8	Ъ—		B8	SB7	ITEM 4
SB4A	1	A 9	├—	-	В9	SB4	ITEM 4
GND(SB3A)	1	AIO	1		BIO	GND(SB3)	ITEM 3
SBA+	1	ALL	144		BII	SBA+	ITEM 3
SBA-	1	A12	₩		B12	SBA-	ITEM 3
GND	1	A13	1 <del>.X.</del>		B13	GND	ITEM 3
Rx2+	1	AI4	₩-		BI4	Tx2+	ITEM 3
R x 2 -	1	A15	₩-		B15	Tx2-	ITEM 3
GND	1	A16	1 <del>.X</del> —		B16	GND	ITEM 3
R x 3+	1	A17	144		817	T x 3+	ITEM 3
R x 3 -	1	A18	1		B18	Tx3-	ITEM 3
GND	1	A19	Ι~	-	B19	GND	ITEM 3, NOTE I
GND	1	BI	1		AI	GND	ITEM 3
Tx0+	1	B2	144		A2	R x 0 +	ITEM 3
T x 0 -	1	B3	₩	-	A3	R x 0 -	ITEM 3
GND	1	B4	1-X-		A4	GND	ITEM 3
TxI+	1	B5	₩-	-	A5	RxI+	ITEM 3
Tx1-	1	B6	1		A6	RxI-	ITEM 3
GND	1	B7	μ_		A7	GND	ITEM 3
SBOA	1	B8	├—	-	A8	SB0	ITEM 4
SBIA	1	B9	Ъ—		A 9	SBI	ITEM 4
GND(SB2A)	1	BIO	<u> </u>	-	A10	GND(SB2)	ITEM 3
SB5A	1	BII	₩-		All	SB5	ITEM 3
SB6A	1	B12	₩-		A12	SB6	ITEM 3
GND	1	B13	1 <del>X</del> –	-	A13	GND	ITEM 3
Tx2+	1	B14	₩-	-	A14	RX2+	ITEM 3
T x 2 -	1	B15	14	-	A15	R x 2 -	ITEM 3
GND	1	B16	1 <del>.X</del>	-	A16	GND	ITEM 3
Tx3+	1	B17	₩-	-	A17	R x 3+	ITEM 3
Tx3-	1	B18	14	-	A18	Rx3-	ITEM 3
GND	1	B19	$\vdash$	-	A19	GND	ITEM 3, NOTE I

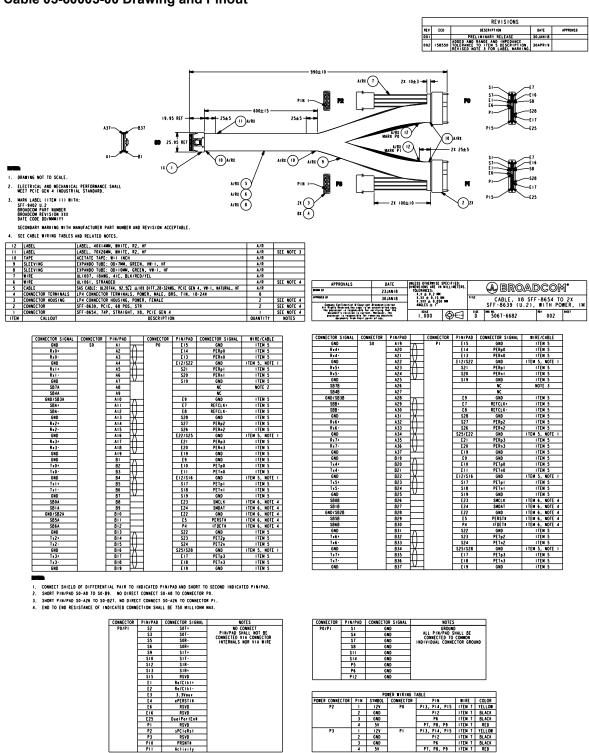
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE
GND	SO	A19	_	PI	BI	GND	ITEM 3, NOTE I
R x 4+	1	A20	144		82	Tx0+	ITEM 3
R x 4 -	1	A21	1111		B3	Tx0-	ITEM 3
GND	1	A22	1 <del>.X</del> —	-	B4	GND	ITEM 3
Rx5+		A23	144-		85	TxI+	ITEM 3
R x 5 -	1	A24	1++-		B6	Tx1-	ITEM 3
GND	1	A25	1ν_		87	GND	ITEM 3
SB7B	1	A26	1—		B8	SB7	ITEM 4
SB4B	1	A27	1—		B9	SB4	ITEM 4
GND(SB3B)	1	A28	1		810	GND(SB3)	ITEM 3
SBB+	1	A29	144—		BII	SBA+	ITEM 3
SBB -	1	A30	₩		812	SBA-	ITEM 3
GND	1	A31	1.X.		B13	GND	ITEM 3
Rx6+	1	A32	₩—		B14	Tx2+	ITEM 3
Rx6-	1	A33	144—		B15	Tx2-	ITEM 3
GND	1	A34	1.X.		B16	GND	ITEM 3
R x 7+	1	A35	144-		B17	Tx3+	ITEM 3
R x 7 -	1	A36	144		B18	Tx3-	ITEM 3
GND	1	A37	$\vdash \vee$		B19	GND	ITEM 3
GND	1	B19	1		Al	GND	ITEM 3, NOTE I
Tx4+	1	B20	144—		A2	R x 0 +	ITEM 3
Tx4-	1	B21	144—		A3	Rx0-	ITEM 3
GND	1	B22	1.X.		A4	GND	ITEM 3
Tx5+	1	B23	144		A5	RxI+	ITEM 3
Tx5-	1	B24	144		A6	RxI-	ITEM 3
GND	1	B25	1V.		A7	GND	ITEM 3
SBOB	1	B26	1—		A8	SB0	ITEM 4
SBIB	1	B27	1		A9	SBI	ITEM 4
GND(SB2B)	1	B28	<b>-</b>		A10	GND(SB2)	ITEM 3
SB5B	1	B29	144-		All	SB5	ITEM 3
SB6B	1	B30	14		A12	SB6	ITEM 3
GND	1	B31	1 <del>.X</del> —		A13	GND	ITEM 3
Tx6+	1	B32	1//		A14	RX2+	ITEM 3
Tx6-	1	B33	₩		A15	Rx2-	ITEM 3
GND	1	B34	1.X.		A16	GND	ITEM 3
Tx7+	1	B35	1//		A17	Rx3+	ITEM 3
Tx7-	i	B36	144—		A18	Rx3-	ITEM 3
GND	1	B37	1V.		A19	GND	ITEM 3

NOTES:
I. PIN/PAD ON CONNECTOR SO SHARED ON CONNECTORS PO AND PI.

#### Cable 05-60005-00

The following figure shows the drawing and pinout for Broadcom cable, 05-60005-00, a x8 SFF-8654 to 2 U.2 SFF-8639 connection.

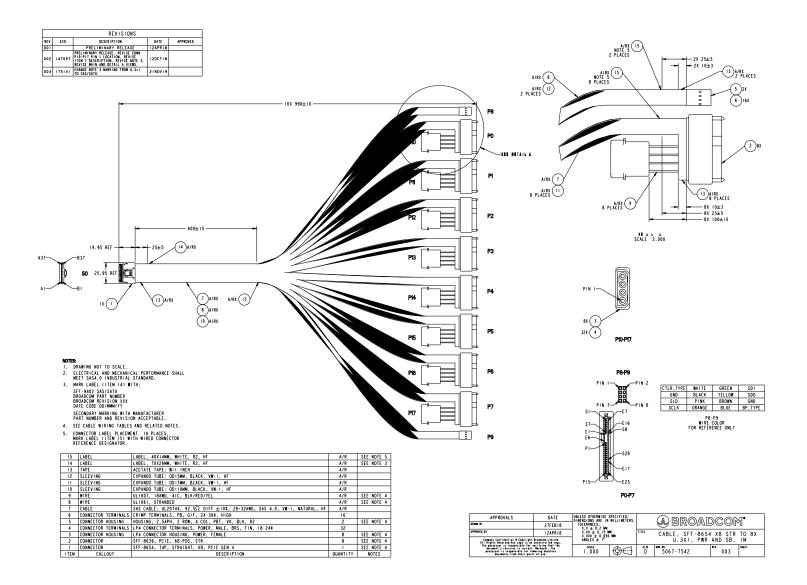
Figure 19: Cable 05-60005-00 Drawing and Pinout



### Cable 05-60006-00

The following figure shows the drawing and pinout for Broadcom cable, 05-60006-00, a x8 SFF-8654 to x8 U.3 SFF-8639 connection.

Figure 20: Cable 05-60006-00 Drawing and Pinout



CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	Al	$\wedge$	P0	\$7	GND	ITEM 7	
R x 0 +	HOST	A2	1//	TARGET	S6	DO_TXO+	ITEM 7	
Rx0-		A3	11 /		\$5	DO_TXO-	ITEM 7	
GND		A4	Ι		\$4	GND	ITEM 7	NOTE 2, NOTE 3
GND		BI	$\vdash_{\wedge}$		\$1	GND	ITEM 7	
T x 0 +		B2	H		\$2	D0_RX0+	ITEM 7	
T x 0 -		B3	$H \rightarrow$		\$3	DO_RXO-	ITEM 7	
GND		B4	$\vdash$		\$4	GND	ITEM 7	NOTE 2, NOTE 3
12V	PIO		_	P0	P13, P14, P15	127	ITEM 9	YELLOW
GND		2	<u> </u>	TARGET	P12	GND	ITEM 9	BLACK
GND		3			P5, P6	GND	ITEM 9	BLACK
5V		4			P7, P8, P9	5V	ITEM 9	RED
NC				P0	PI	RSVD		NOTE 4
NC			느느	TARGET	P2	s PC I e R s t		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	A4	_	PI	\$7	GND	ITEM 7	NOTE 2
Rx I+	HOST	A5	1//	TARGET	S 6	DI_TXO+	ITEM 7	
RxI-		A6	Ш		\$5	DI_TXO-	ITEM 7	
GND		A7	W_		\$4	GND	ITEM 7	NOTE 3
GND	1	84			\$1	GND	ITEM 7	NOTE 2
Tx I +		B5	1//		\$2	DI_RXO+	ITEM 7	
TxI-		B6	ш.		\$3	DI_RXO-	ITEM 7	
GND	1	B7	Ιν_		\$4	GND	ITEM 7	NOTE 3
12V	PII	- 1		PI	PI3, PI4, PI5	127	ITEM 9	YELLOW
GND		2	<b>—</b>	TARGET	P12	GND	ITEM 9	BLACK
GND		3	<u> </u>		P5, P6	GND	ITEM 9	BLACK
5V		4	<b>—</b>		P7, P8, P9	5V	ITEM 9	RED
NC				PI	PI	RSVD		NOTE 4
NC				TARGET	P2	sPC   eRs t		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	SO	A13		P2	S7	GND	ITEM 7	MOTES
Rx2+	HOST	AI4	Ω	TARGET	\$6	D2_TX0+	ITEM 7	
Rx2-		A15			\$5	D2_TX0-	ITEM 7	
GND		A16	W		S4	GND	ITEM 7	NOTE 2, NOTE 3
GND		BI3			SI	GND	ITEM 7	MOTE Z, MOTE 3
Tx2+		B14	Δ		\$2	D2_RX0+	ITEM 7	
Tx2-		B15			\$3	D2_RX0-	ITEM 7	
GND		B16	V		\$4	GND	ITEM 7	NOTE 2. NOTE 3
127	P12	1		P2	PI3, PI4, PI5	127	ITEM 9	YELLOW
GND	' ' '	2		TARGET	P12	GND	ITEM 9	BLACK
GND		3			P5, P6	GND	ITEM 9	BLACK
5V		4			P7. P8. P9	5V	ITEM 9	RED
NC NC				P2	P1	RSVD	112 11 3	NOTE 4
NC NC				TARGET	P2	sPC LeRs t		NOTE 4
L nt					1 12	arcienst.		INVIE 4

CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND Rx6+	S0 HOST	A31 A32	Λ	P6 TARGET	\$7 \$6	GND	ITEM 7	
	1			17111021		D6_TX0+		
R×6-	4	A33	17	1	\$5	D6_TXO-	ITEM 7	
GND	4	A34		1	\$4	GND	ITEM 7	NOTE 2, NOTE 3
GND	4	B31	$\Lambda$	1	\$1	GND	ITEM 7	
T x 6 +	4	B32		1	\$2	D6_RX0+	ITEM 7	
T x 6 -	4	B33	1		\$3	D6_RX0-	ITEM 7	
GND		B34			\$4	GND	ITEM 7	NOTE 2, NOTE 3
127	PI6			P6 TARGET	P13, P14, P15	12V	ITEM 9	YELLOW
GND	1	2		IAROLI	P12	GND	ITEM 9	BLACK
GND	1	3			P5, P6	GND	ITEM 9	BLACK
5V		4			P7, P8, P9	5V	ITEM 9	RED
NC	1			P6 TARGET	PI	RSVD		NOTE 4
NC				IAKULI	P2	sPCleRst		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	A34	$\overline{}$	P7	\$7	GND	ITEM 7	NOTE 2
R x 7+	HOST	A35	1//	TARGET	\$6	D7_TX0+	ITEM 7	
R x 7 -	1	A36	1		\$5	D7_TX0-	ITEM 7	
GND	1	A37	V		\$4	GND	ITEM 7	NOTE 3
GND	1	B34	_		\$1	GND	ITEM 7	NOTE 2
Tx7+	1	B35	1/1		\$2	D7_RX0+	ITEM 7	
Tx7-	1	B36	ш		\$3	D7_RX0-	ITEM 7	
GND	1	B37	V		\$4	GND	ITEM 7	NOTE 3
127	P17	i		P7	P13, P14, P15	127	ITEM 9	YELLOW
GND	1	2		TARGET	P12	GND	ITEM 9	BLACK
GND	1	3			P5. P6	GND	ITEM 9	BLACK
5V	1	4			P7, P8, P9	5V	ITEM 9	RED
NC				P7	PI	RSVD		NOTE 4
NC	1		1 L	TARGET	P2	sPC LeRs t		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
SB7A	S0 HOST	A8		P8	8	BP_TYPEA	ITEM 8	NOTE I, BLUE
SB4A	1031	A 9		P8	4	SDOA	ITEM 8	NOTE I, YELLOW
GND/SB3A	4	A10		P8	6	GND	ITEM 8	NOTE I, BROWN
SBA+		All	4			NC		
SBA-		A12				NC		
SBOA	1	B8		P8	7	SCLKA	ITEM 8	NOTE I, ORANGE
SBIA	1	B9		P8	5	SLDA	ITEM 8	NOTE I, PINK
GND/SB2A	1	BIO		P8	3	GND	ITEM 8	NOTE I, BLACK
SB5A		BII		P8	2	SDIA	ITEM 8	NOTE I, GREEN
				P8	1 1	CTLR_TYPEA	ITEM 8	NOTE I, WHITE
SB6A		B12						
SB 7B	SO.	A26		P9	8	BP_TYPEB	ITEM 8	NOTE I, BLUE
SB 7B SB 4B	S0 HOST	A26 A27		P9	4	SDOB	ITEM 8	NOTE I, YELLOW
SB 7B SB 4B GND / SB 3B		A26 A27 A28		P9		SDOB GND	ITEM 8	NOTE I, BLUE NOTE I, YELLOW NOTE I, BROWN
SB 7B SB 4B GND / SB 3B SBB+		A26 A27 A28 A29		P9	4	SDOB GND NC	ITEM 8	NOTE I, YELLOW
SB7B SB4B GND/SB3B SBB+ SBB-		A26 A27 A28 A29 A30		P9 P9 P9	4 6	SDOB GND NC NC	ITEM 8 ITEM 8 ITEM 8	NOTE I, YELLOW NOTE I, BROWN
SB7B SB4B GND/SB3B SBB+ SBB- SB0B		A26 A27 A28 A29 A30 B26		P9 P9 P9	6	SDOB GND NC NC SCLKB	ITEM 8 ITEM 8 ITEM 8	NOTE I, YELLOW NOTE I, BROWN NOTE I, ORANGE
\$B7B \$B4B \$BD/\$B3B \$BB+ \$BB- \$B0B \$B1B		A26 A27 A28 A29 A30 B26 B27		P9 P9 P9	7 5	SDOB GND NC NC SCLKB SLDB	ITEM 8 ITEM 8 ITEM 8 ITEM 8 ITEM 8	NOTE I, YELLOW NOTE I, BROWN NOTE I, ORANGE NOTE I, PINK
SB7B SB4B GND/SB3B SBB+ SBB- SB0B SB1B GND/SB2B		A26 A27 A28 A29 A30 B26 B27 B28		P9 P9 P9 P9 P9	7 5 3	SDOB GND NC NC SCLKB SLDB GND	ITEM 8	NOTE I, YELLOW NOTE I, BROWN NOTE I, ORANGE
SB7B SB4B GND/SB3B SBB+ SBB- SB0B SB1B		A26 A27 A28 A29 A30 B26 B27		P9 P9 P9	7 5	SDOB GND NC NC SCLKB SLDB	ITEM 8 ITEM 8 ITEM 8 ITEM 8 ITEM 8	NOTE I, YELLOW NOTE I, BROWN NOTE I, ORANGE NOTE I, PINK

			_				I	
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD	_	CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	
GND	SO HOST	A16	$\Lambda$	P3 TARGET	\$7	GND	ITEM 7	NOTE 2
Rx3+	11001	A17		1 TAMOL I	\$6	D3_TX0+	ITEM 7	
Rx3-		A18	1	1	\$5	D3_TXO-	ITEM 7	
GND		A19	-	1	\$4	GND	ITEM 7	NOTE 2, NOTE 3
GND		B16	$\Lambda$	1	\$1	GND	ITEM 7	NOTE 2
Tx3+		B17		1	\$2	D3_RX0+	ITEM 7	
Tx3-	ļ	B18	1	1	\$3	D3_RXO-	ITEM 7	
GND		B19	-		\$4	GND	ITEM 7	NOTE 2, NOTE :
127	P13	1		P3 TARGET	P13, P14, P15	120	ITEM 9	YELLOW
GND	ļ	2		TANGET	P12	GND	ITEM 9	BLACK
GND		3		1	P5, P6	GND	ITEM 9	BLACK
5V		4			P7, P8, P9	5V	ITEM 9	RED
NC				P3 TARGET	PI	RSVD		NOTE 4
NC			_	TAROLI	P2	s PC I e R s t		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	A19	_	P4	\$7	GND	ITEM 7	NOTE 2
Rx4+	HOST	A20	1//	TARGET	S 6	D4_TX0+	ITEM 7	
R x 4 -	1	A21	ш		\$5	D4_TXO-	ITEM 7	
GND	1	A22	ΙV		S4	GND	ITEM 7	NOTE 2, NOTE
GND	1	B19	<del>    </del>		SI	GND	ITEM 7	NOTE 2
Tx4+	1	B20	1//		\$2	D4_RX0+	ITEM 7	
T x 4 -	1	B21	11.	-	\$3	D4_RX0-	ITEM 7	
GND	1	B22	1	-	\$4	GND	ITEM 7	NOTE 2, NOTE :
12V	P14	- 1		P4	P13, P14, P15	127	ITEM 9	YELLOW
GND	1	2	├—	TARGET	P12	GND	ITEM 9	BLACK
GND	1	3	├──	1	P5, P6	GND	ITEM 9	BLACK
5V	]	4	├──	1	P7, P8, P9	5V	ITEM 9	RED
NC				P4	PI	RSVD		NOTE 4
NC				TARGET	P2	s PC T e R s t		NOTE 4
CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	SO	A22		P5	S7	GND	ITEM 7	NOTE 2
Rx5+	HOST	A23	ŁΔ	TARGET	S6	D5_TX0+	ITEM 7	NOTE 2
Rx5-	1	A24	ш		\$5	D5_TX0-	ITEM 7	
GND	1	A25	LV_	]	S4	GND	ITEM 7	NOTE 3
GND	1	B22		]	SI	GND	ITEM 7	NOTE 2
Tx5+	1	B23	1//		\$2	D5_RX0+	ITEM 7	
Tx5-	1	B24	ш		\$3	D5_RXO-	ITEM 7	
GND	1	B25	$\perp$		S4	GND	ITEM 7	NOTE 3
127	P15	I		P.5	P13. P14. P15	127	ITEM 9	YFILOW
GND	1	2	<u> </u>	TARGET	P12	GND	ITEM 9	BLACK
GND	1	3	<u> </u>		P5, P6	GND	ITEM 9	BLACK
5V	1	4	<u> </u>		P7. P8. P9	5V	ITEM 9	RED
NC NC		<u> </u>		P5	P1	RSVD		NOTE 4
NC NC	1		1 L	TARGET	P2	sPC LeRs t		NOTE 4
""						31010101		1 1012 4

CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	NOTES
P0-P7	\$9	\$17+	NO CONNECT
TARGET	\$10	SIT-	PIN/PAD SHALL NOT BE CONNECTED VIA CONNECTOR
	\$12	SIR-	INTERNALS NOR VIA WIRE
	\$13	SIR+	
	\$17	\$2T+	
	\$18	\$2T-	
	\$20	S2R-	
	\$21	S2R+	
L	\$23	\$3T+	
L	\$24	\$31 -	
L	S26	S3R-	
L	\$27	S3R+	
L	P3	PWRDIS	
L	P4	IFDET#	
	PIO	PRSNT#	
	PII	Activity/DisableStaggeredSpinup	
	ΕI	RefClk+	
	E 2	RefClkl-	
	E3	3.3Vaux	
	E 4	ePERSTI#	
L	E 5	ePERSTO#	
L	E 6	IFDET2#	
L	E 7	RefCIk0+	
	E8	RefCIk0-	
	E 9	GND	
	EIO	PETp0	
	EII	PETn0	
	E12	GND	
	E13	PERn0	
	E 1 4	PERp0	
	E15	GND	
	E16	HPTI	
	E17	PETp3	
L	E18	PETn3	
	E19	GND	
	E20	PERn3	
	E21	PERp3	
	E 2 2	GND	
	E23	SMC I k	
	E24	SMDat	
	E25	DualPortEn#	
P0-P7	\$8	GND	GROUND
TARGET	\$11	GND	ALL PIN/PAD SHALL BE
·····	\$14	GND	CONNECTED TO COMMON
	S14 S15		INDIVIDUAL CONNECTOR GROUND
	S16	GND (HPTO)	
	\$19	GND GND	
	S22	GND	
	S22 S25	GND	
	S28	GND	
	328	GNU	

- NOTES:

  I. END TO END RESISTANCE OF INDICATED CONNECTION SHALL BE 750 MILLIOHM MAX.

  2. SHARED PIN/PAD ON HOST (50) CONNECTOR.

  3. SHARED PIN/PAD PI TO P2 OF INDICATED TARGET (P0-P7) CONNECTOR.

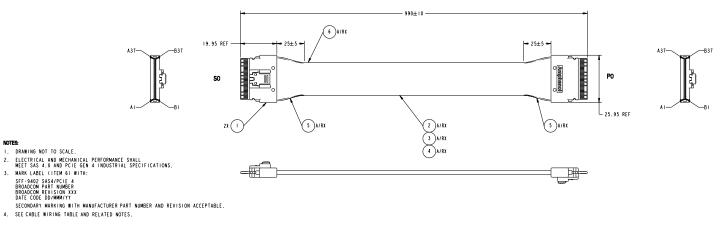
  4. SHORT PIN/PAD PI TO P2 OF INDICATED TARGET (P0-P7) CONNECTOR. NO CONNECT TO OTHER CONNECTORS.

## Cable 05-60007-00

The following figure shows the drawing and pinout for Broadcom cable, 05-60007-00, a x8 SFF-8654 to x8 SFF-8654 connection.

Figure 21: Cable 05-60007-00 Drawing and Pinout

	REVISIONS									
REV	ECO	DESCRIPTION	DATE	APPROVED						
001		PRELIMINARY RELEASE	0 I MAY I 8							
002	175141	ADD WIRE GAUGE AND IMPEDANCE TOLERANCE ITEM 2. REVISE MARKING REQUIREMENTS NOTE 3.	2 I NOV I 9							



6	LABEL	LABEL, 70x26MM, WHITE, R2, HF	A/R	SEE NOTE 3
5	TAPE	ACETATE TAPE: W=1 INCH	A/R	
4	SLEEVING	EXPANDO TUBE: OD=10MM, GREEN, VW-I, HF	A/R	
3	WIRE	ULIO61, STRANDED	A/R	SEE NOTE 4
2	CABLE	SAS CABLE: UL20744, 28-32AWG, 92.5Ω ±10% DIFF, SAS4, PCIE GEN 4, VW-1, NATURAL, HF	A/R	
	CONNECTOR	SFF-8654, 74P, STRAIGHT, X8, STANDARD, SAS 4.0, PCIE GEN 4	2	SEE NOTE 4
ITEM	CALLOUT	DESCRIPTION	QUANTITY	NOTES

APPROVALS DRAME BY	DATE 15FEB18	UNLESS OTHERWISE DIMENSIONS ARE IN TOLERANCES: X.X ± 0.2 MM	SPECIFIED: MILLIMETERS.		@BROADCOM*		
APPROVED BY  Company Confidential © Copyright All Rights Reserved Any copy is an	X:XX ± 0.15 b X:XXX ± 0.050 ANGLES ± 1		TITLE	CABLE, SFF-8654 TO SFF-8654, X8, STRAIGHT, IM			
The possessor is responsible for a document's revision is current possessor is responsible for re documents from their poin	2.000	⊕€	SIZE D	5067-6869	002	SHEET	

CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	AI	$\sim$	P0	BI	GND	ITEM 3	
Rx0+	1	A2	ш	ł	B2	T x 0+	ITEM 3	
Rx0-	]	A3	₩	1	B3	T x 0 -	ITEM 3	
GND	1	A4	<del>  X -</del>	ł	B4	GND	ITEM 3	
RxI+	1	A5	Н—	ł	B5	TxI+	ITEM 3	
RxI-		A6	₩	ł	B6	Tx1-	ITEM 3	
GND	1	A7	Ιν_		B7	GND	ITEM 3	
SB7A	1	A8	├──	ł	B8	SB7A	ITEM 4	NOTE I
SB4A	1	A9	├—	-	B9	SB4A	ITEM 4	NOTE I
GND/SB3A	1	A I O	-	Į.	BIO	GND/SB3A	ITEM 3	
SBA+	1	AII	ш.		BII	SBA+	ITEM 3	
SBA-	1	A12	₩		B12	SBA-	ITEM 3	
GND	1	A13	1-X-	ļ	B13	GND	ITEM 3	
Rx2+	1	A14	144		B14	Tx2+	ITEM 3	
Rx2-	1	A15	1		B15	Tx2-	ITEM 3	
GND	1	A16	IX—	ļ	B16	GND	ITEM 3	
Rx3+	1	A17	144		B17	T x 3+	ITEM 3	
Rx3-	1	A18	₩.	ļ	B18	Tx3-	ITEM 3	
GND	1	A19	IX—		B19	GND	ITEM 3	
Rx4+	1	A20	141		B20	T x 4+	ITEM 3	
Rx4-	1	A21	₩.	ļ	B21	T x 4 -	ITEM 3	
GND	1	A22	IX-		B22	GND	ITEM 3	
Rx5+	1	A23	144	ļ	B23	Tx5+	ITEM 3	
Rx5-	1	A24	₩.		B24	T×5-	ITEM 3	
GND	1	A25	$\mathcal{V}$		B25	GND	ITEM 3	
SB7B	1	A26		ļ	B26	SB7B	ITEM 4	NOTE I
SB4B	1	A27			B27	SB4B	ITEM 4	NOTE I
GND/SB3B	1	A28	<del>  </del>		B28	GND/SB3B	ITEM 3	
SBB+	1	A29	144	ļ	B29	SBB+	ITEM 3	
SBB-	1	A30	1		B30	SBB -	ITEM 3	
GND	1	A31	I.X.	ļ	B31	GND	ITEM 3	
Rx6+	1	A32	1/1	ļ	B32	Tx6+	ITEM 3	
Rx6-	1	A33	Ш		B33	Tx6-	ITEM 3	
GND	1	A34	1.X.	l	B34	GND	ITEM 3	
Rx7+	1	A35	ш.	1	B35	T x 7+	ITEM 3	
Rx7-	1	A36	Ш		B36	Tx7-	ITEM 3	
GND	1	A37	$\mathcal{V}$	1	B37	GND	ITEM 3	

CONNECTOR SIGNAL	CONNECTOR	PIN/PAD		CONNECTOR	PIN/PAD	CONNECTOR SIGNAL	WIRE/CABLE	NOTES
GND	S0	BI	-	P0	Al	GND	ITEM 3	
T x 0+		B2	н –	1	A2	Rx0+	ITEM 3	
T x 0 -		B3	H +		A3	R x 0 -	ITEM 3	
GND		B4	<del>Ж</del>	1	A4	GND	ITEM 3	
T x I +		B5	н-	1	A5	Rx1+	ITEM 3	
TxI-		B6	₩	-	A6	RxI-	ITEM 3	
GND		B7	$\vdash$	-	A7	GND	ITEM 3	
SBOA		B8	$\vdash$	-	A8	SBOA	ITEM 4	NOTE I
SBIA	1	B9	1—		A 9	SBIA	ITEM 4	NOTE I
GND/SB2A	1	BIO	-		AIO	GND/SB2A	ITEM 3	
SB5A	1	BII	₩-		AII	SB5A	ITEM 3	
SB6A	1	B12	11		A12	SB6A	ITEM 3	
GND	1	B13	1X —		A13	GND	ITEM 3	
Tx2+	1	BI4	₩-		A14	Rx2+	ITEM 3	
Tx2-	1	B15	11.		A15	Rx2-	ITEM 3	
GND	1	B16	1X—		A16	GND	ITEM 3	
Tx3+	1	B17	1//		A17	Rx3+	ITEM 3	
Tx3-	1	B18	11.		A18	Rx3-	ITEM 3	
GND	1	B19	IX—		A19	GND	ITEM 3	
T x 4+	1	B20	1//		A20	Rx4+	ITEM 3	
T x 4 -	1	B21	11		A21	Rx4-	ITEM 3	
GND	1	B22	1X-		A22	GND	ITEM 3	
T x 5+	1	B23	144—		A23	Rx5+	ITEM 3	
Tx5-	1	B24	₩		A24	Rx5-	ITEM 3	
GND	1	B25	1ν_		A25	GND	ITEM 3	
SBOB	1	B26	ऻ		A26	SB0B	ITEM 4	NOTE I
SBIB	1	B27	├—		A27	SBIB	ITEM 4	NOTE I
GND/SB2B	1	B28	1		A28	GND/SB2B	ITEM 3	
SB5B	1	B29	1//		A29	SB5B	ITEM 3	
SB6B	1	B30	14		A30	SB6B	ITEM 3	
GND	1	B31	1X—		A31	GND	ITEM 3	
Tx6+	1	B32	144		A32	Rx6+	ITEM 3	
Tx6-	1	B33	14		A33	Rx6-	ITEM 3	
GND	1	B34	1.X.		A34	GND	ITEM 3	
Tx7+	i	B35	₩.		A35	Rx7+	ITEM 3	
Tx7-	1	B36	11.		A36	Rx7-	ITEM 3	
GND	1	B37	IV.		A37	GND	ITEM 3	

NOTES:

1. END TO END RESISTANCE OF INDICATED CONNECTION SHALL BE 750 MILLIOHM MAX.

## **Revision History**

#### Version 2.10, December 20, 2023

- Updated the following layout figures in Broadcom MegaRAID and HBA Tri-Mode Storage Adapter Characteristics: 9500-8i, 9560-16i, 9560-8i, 9540-8i, 9500-16i.
- Added FCC Compliance.

#### Version 2.9, April 6, 2023

Updated the adapter dimensions in Broadcom MegaRAID and HBA Tri-Mode Storage Adapter Characteristics.

#### Version 2.8, February 7, 2023

Added NVMe support to the MegaRAID 9540-8i adapter.

#### Version 2.7, November 22, 2022

Added VCCI – Japan.

#### Version 2.6, August 29, 2022

- Revised RAID levels in RAID Features.
- Updated the 05-60006-00 description in Storage Interface Cabling.

#### Version 2.5, January 13, 2022

- Updated the values regarding the CacheVault power module in MegaRAID Tri-Mode Storage Adapter Power Supply Requirements.
- Added Adapter Security.
- Updated HBA model information in Marks, Certifications, and Compliance.
- Added the MegaRAID 9562-16i and HBA 9502-16i OCP adapters.
- Added the MegaRAID 9540-8i adapter.

#### Version 2.4, August 25, 2020

- Updated the Sideband Signal Pinout table in Sideband Signals.
- Updated CacheVault Data Protection.

#### Version 2.3, May 1, 2020

• Updated PCIe (NVMe) Support.

#### Version 2.2, March 17, 2020

- Added connector identifiers to the drawings in Broadcom MegaRAID and HBA Tri-Mode Storage Adapter Characteristics.
- Template update.

#### **Version 2.1, February 12, 2020**

- Changed the relative humidity range and temperature range for operating conditions in Operating and Nonoperating Conditions.
- Updated the cable drawing in Cable 05-60002-00.
- Added Cable 05-60006-00 and Cable 05-60007-00.

#### Version 2.0, December 17, 2019

- Updated Operating System Support.
- · Removed Backplanes with Mini-SAS HD Connectors.
- Updated Figure 5.
- Added External Adapter Connector Pinout, Virtual Pin Port Management, Sideband Signals, and Cable Drawings and Pinouts.
- Updated board dimensions in HBA 9500-16e Adapter Connector and LED Designations, and HBA 9500-8e Adapter Connector and LED Designations.
- Updated connector and LED locations in MegaRAID 9580-8i8e Adapter Connector and LED Designations.

#### Preliminary, Version 1.0, September 27, 2018

Initial document release.

